

Jiawen Chen

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Home

70 Pacific Street, #430
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Experience

Massachusetts Institute of Technology, Cambridge, MA
Computer Science and Artificial Intelligence Laboratory, Graphics Group
Graduate Research Assistant (September 2004 - Present)
Thesis (in progress): *Efficient Data Structures for Representing
Piecewise-smooth Visual Information*
Undergraduate Research Assistant (May 2002 - Dec 2002, Fall 2003)
Media Laboratory, Sociable Media Group
Undergraduate Research Assistant (Spring 2002)

Adobe Systems, Inc., *Advanced Technology Lab*, Newton, MA
Graphics and Vision Research Intern (Summer 2008)

Adobe Systems, Inc., *Graphics Research Group*, Newton, MA
Graphics and Vision Research Intern (Summer 2007)

Apple Computer, Inc., *Graphics and Imaging Group*, Cupertino, CA
Software Engineering Intern (Summer 2005)

Google, Inc., *Google Desktop*, Mountain View, CA
Software Engineering Intern (Summer 2004)

Microsoft Corp., *Netgen Group*, Redmond, WA
Software Design Engineer Intern (Summer 2003)

National High Magnetic Field Laboratory,
Electron Paramagnetic Resonance Group, Tallahassee, FL
Undergraduate Research Assistant (Summer 2001)

Education

Massachusetts Institute of Technology, Cambridge, MA
PhD. Candidate: Computer Science (Sept 2005 - Present)
Advisor: Fredo Durand

Massachusetts Institute of Technology, Cambridge, MA
Master of Engineering: Electrical Engineering and Computer Science (2005)
Advisor: Fredo Durand
Thesis: *Load-balanced Rendering on a General-Purpose Tiled Architecture*

Massachusetts Institute of Technology, Cambridge, MA
Bachelor of Science: Electrical Engineering and Computer Science (2004)
Bachelor of Science: Physics (2004)
Minor: Mathematics

Stuyvesant High School, Class of 2000, New York, NY

- Publications** *Real-time Edge-Aware Image Processing with the Bilateral Grid*
Jiawen Chen, Sylvain Paris and Fredo Durand
SIGGRAPH 2007
- Texture Transfer Using Geometry Correlation*
Tom Mertens, Jan Kautz, Jiawen Chen, Philippe Bekaert and Fredo Durand
Eurographics Symposium on Rendering 2006
- A Reconfigurable Architecture for Load-Balanced Rendering*
Jiawen Chen, Michael I. Gordon, William Thies, Matthias Zwicker,
Kari Pulli and Fredo Durand
Graphics Hardware 2005
- Pervasive Pose-Aware Applications and Infrastructure*
Seth Teller, Jiawen Chen and Hari Balakrishnan
IEEE Computer Graphics and Applications, Vol. 23, No. 4. 2003
- Fellowships** National Science Foundation Graduate Research Fellowship (2005-2007)
NVIDIA Fellowship (2005)
- Professional Activities** Reviewer: SIGGRAPH, Eurographics, Computer Graphics Forum,
The Visual Computer, IEEE Transactions on Image Processing,
Applied Optics, IEEE Symposium on Circuits and Systems
Member: ACM (Student), Eta Kappa Nu, Tau Beta Pi
Invited Talks: MIT Advanced Computer Graphics Course (2006)
- Service** MIT IS&T Residential Computing Consultant (2003-2006)
Sidney-Pacific Graduate Residence: IT Chair (2006)
Sidney-Pacific Graduate Residence: Webmaster (2005)
- Personal** Citizenship: United States
Languages Spoken: English (fluent), Mandarin Chinese (proficient)