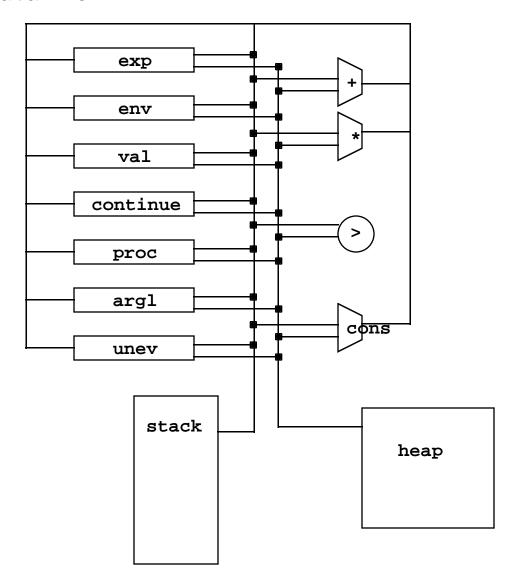
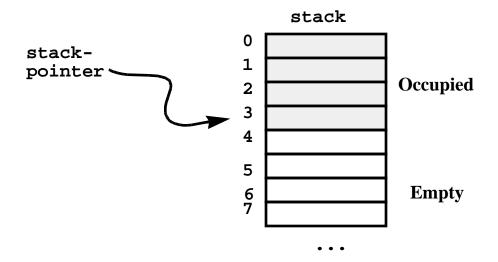
Scheme Interpreter - Register Machine

Data Flow:



Vector Implementation of the Stack



Implementing the Pair Abstraction

Pair accessors:

Pair allocation:

Mark/Sweep

Mark Phase:

- If pair
 - If already marked, then return
 - else
 - -- set the mark
 - -- mark car
 - -- mark cdr
- Else not a pair, so return

Sweep Phase:

- Set free to E0
- Start scanning at end of memory
- Scan loop
 - If scan pointer is before start of memory, then we're done
 - If mark set for scan cell
 - -- clear mark
 - -- move scan pointer back one
 - -- continue at scan loop
 - Else mark is not set
 - -- set cdr of scan cell to free
 - -- Set free to scan
 - -- move scan pointer back one
 - -- continue at scan loop

Stop & Copy

Two Parts:

- 1. Move cells in old memory to front of new memory
- 2. Update pointers in new memory to point to *new* locations of cells

Sweep Phase:

- Set free to E0
- Start scanning at end of memory
- Scan loop
 - If scan pointer is before start of memory, then we're done
 - If scan cell already marked, then return
 - If mark set for scan cell
 - -- clear mark
 - -- move scan pointer back one
 - -- continue at scan loop
 - Else mark is not set
 - -- set cdr of scan cell to free
 - -- Set free to scan
 - -- move scan pointer back one
 - -- continue at scan loop