MULTICS WORKING NOTES

Herein described is a method of aborting or terminating an IMCV from the processor maintenance panel in such a way that a normal return-tape is produced. In contradistinction, a TERM entered at the operator's console produces no return-tape whatever.

The procedure is as follows:

a. Set the step enable dial to 'prog'.

b. Check to see that the processor is not executing segment 5. If the processor is not executing segment 5, proceed with step #c. If the processor is executing segment 5, turn the step enable dial to 'off' and poke the step button. Repeat steps #a and #b, until the processor is out of segment 5.

c. Set the execute switches* active (up).

d. Key the following instruction into the instruction switches:
   7 00000 1731 00 (ldb sb | 0)

e. Poke the execute button once.

f. Poke the step button once.

g. Key the following instruction into the instruction switches:
   7 00010 0731 00 (lreg sb | 8)

h. Poke the execute button once.

i. Poke the step button once.

j. Key the following instruction into the instruction switches:
   7 00024 6101 00 (rtcd sb | 20)

k. Poke the execute button once.

l. Poke the step button once.

m. Set the execute switches inactive (down).

n. Set the step enable dial to 'off'.
o. Poke the step button once.

After step #o, the user's program has effectively done a 'return' back to the system, just as though it had terminated of its own accord.

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*Note: There is exactly one switch labeled execute switches.