Identification

Correction to PL/I built-in clock function
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Correction

The following section should be inserted in place of the similarly titled paragraph in BP.0.03:

Real Time Clock

A real time clock is attached to the GE645. Executing the machine instruction RCCL, with the appropriate effective address, will load the A-Q registers with a 72 bit integer which is the number of microseconds since 0000 GMT, January 1, 1901. The built-in abnormal function "clock", with no arguments, should return a 71 bit fixed binary number as its value. This function should compile the single instruction,

        RCCL <clock_>|[clock_],*

The system will initialize the segment <clock_> to contain the appropriate effective address for the RCCL instruction. For example, the statements,

        dcl x fixed binary (71);
        x = clock;

should compile as

        RCCL <clock_>|[clock_],*
        STAQ x