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The attached revision of BD.7.02 contains the following changes:

- Fields for ring crossing information and subsystem ID are defined in the stack frame.
- 2. The argument descriptions are further defined.
- 3. Alternate returns have been deleted and abnormal returns added.

MULTICS SYSTEM-PROGRAMMERS * MANUAL

SECTION BD.7.02 PAGE 1

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Identification

CALL, SAVE, and RETURN Sequences for Ordinary Slave Procedures R. Montrose Graham

Purpose

Because of the complexity of the hardware, the linkage mechanism, and the stack manipulations, standard call, save, and return sequences for ordinary slave procedures have been adopted (for execute-only and master procedures see section BD.7.03). All translators (including assemblers) must generate these sequences. All supervisor entries, commands, and public library procedures will use these sequences; further, they will assume that other procedures use them.

Stack Usage

The standard call, save, and return sequences use the stack. The usage of the various locations in the stack is shown in the diagram. For the purpose of the diagram alpha calls beta, beta calls gamma, and gamma calls delta. Each time a procedure is called, the value of the stack pointer sp (the paired bases $sb \leftarrow sp$) is adjusted to point to a new region of the stack segment, called a stack frame, which the called procedures uses. The value of sp must equal zero (modulo 8) since the instructions for storing the bases and registers require addresses which equal zero (modulo 8). When the procedure beta is executing the base pair $sb \leftarrow sp$ will point to sp beta (in the diagram). The cross_ring_flag and cross_ring_pointer are used by the protection and abnormal return mechanism (see BD.9.01, BD.9.05). Conceptually there is a single stack per process. All of the active frames in the stack are connected by two threads: a forward thread (sp[18) and a backward thread (sp[16). Actually there is a stack per ring. The frames within a stack are threaded together. The cross_ring_flag, if equal to 1, indicates that control left and reentered this ring immediately preceding that frame, and the cross_ring_pointer indicates where control came from on reentry to this ring. The subsystem ID is an optional 18-bit field available for use by subsystems to further identify this frame.



MULTICS SYSTEM-PROGRAMMERS MANUAL SECTION BD.7.02 PAGE 3

Argument Lists

An argument list is a sequence of pointers (its pairs) preceded by a count of the number of arguments. Specifically, the form is:

arglist



<u>n</u> is the number of arguments. <u>arglist + 2*i</u> and <u>arglist + 2*i</u> + 1 contain an <u>its</u> pair which points to the ith argument (or points to a pointer if the modifier is non-zero). MULTICS SYSTEM-PROGRAMMERS MANUAL SECTION BD.7.02 PAGE 4

The ith argument (or pointer to it) is located at $\langle si \rangle | ai$, i.e., at relative location ai in segment $\langle si \rangle$, whose segment number is si# (see BD.7.00 for description of notation). The argument list must begin at an even location.

If the right half of <u>arglist</u> equals 2, <u>sp</u> value is the value of the stack pointer for the last generation of storage associated with the procedure being called. This is used for a call to an <u>internal</u> procedure which is, in any way, involved in intersegment communication. For example, any call to a procedure entry point which has been passed as a procedure parameter uses this option. The value to put in <u>sp</u> value, in this case, is the second <u>its</u> pair in the procedure parameter. If the right half of <u>arglist</u> is zero, there is no <u>sp</u> value.

If the left half of arglist + 1 is 2*n, a list of pointers to argument descriptions is appended to the argument list. A description of the ith argument is found starting at <ti>|bi. In general, this description will be in the symbol table for the procedure (see BD.1.00), however it may be anywhere. This descriptive information is used in such cases as the callback of the protection mechanism (see BD.9.01) and interprocess calls.

As a minimum requirement, these pointers must point to a word which has the format,



where data_type is a code describing the argument and input_output_type = 1 if the argument is input only for the called procedure

= 2 if the argument is both input and output for the called procedure
= 0 if the input/output type of the argument is unknown

The interpretation of data_type is defined in BD.1.00. Only those argument types described in BB.2 (scalars and 1-dimensional arrays of scalars) are normally supported in user interfaces to the system. While the above is all that is required, the established convention is that these pointers point to a full segment symbol table entry for the argument. MULTICS SYSTEM-PROGRAMMERS MANUAL SECTION BD.7.02 PAGE 5

Ca11

The standard calling sequence is:

stb	sp 0	save bases
sreg	sp 8	save registers
eapap	arglist	establish argument pointer
stcd	sp]20	save return
tra	entrypoint	transfer to called procedure

<u>arglist</u> is the location of the argument list. There is no restriction on the type of address which may be used for arglist. The argument list must be generated before the calling sequence is executed. <u>entrypoint</u> is the entry point of the procedure being

called. entrypoint may be any type of address.

<u>Save</u>

The save sequence is distributed between the called procedure and the called procedure's linkage section. Let <lib>[[entry] be the entry point of the called procedure, then the following portion of the save sequence appears in the linkage section of <lib> at <lib.link> in:

in -*,ic eaplp established linkage pointer inx-*, ic* transfer to procedure segment tra . . .

in>

×	lib#	its
	entry	0

points to actual

procedure entry

The remainder of the save sequence appears in the procedure segment itself at <lib>[[entry]:

entry

/	eapbp	sp 18*	est a blish new, last sp
	eapbp	bp/t bp/t	establish new, next sp
	eabsp stpap	bp -t sp 26	establish stack pointer save ap for this call

The constant t=0 (modulo 8) and must be greater than or equal to 32. t is the number of stack words needed for temporary storage by the called procedure, however in the above sequence t must be less than 2**14. If more space than that is needed the above save sequence may be optionally suffixed by,

adbbp	xt,du
stpbp	sp 18

where xt may be any 18-bit constant. The total amount of stack reserved will then be t+xt. The definition, in the linkage section of <lib>, of the symbol <u>entry</u> is marked so that the linker will not establish a link directly to the procedure segment <lib>, but to its linkage section at <lib.link> |in (see BD.7.01 for details). After completion of the save, sp (the base pair sb sp) points to the beginning of the region, in the stack for use by the called routine, lp (the base pair lb links) points to the beginning of called routine's linkage section, and ap (the base pair ab ap) points to the beginning of the argument list for this call.

The save sequence displayed above has a critically important, although non-obvious, property. At all times sp|18 contains a valid <u>its</u> pair pointing to the top of the stack. The save sequence prepares the next frame <u>before</u> loading sp with a pointer to the new frame. This property is required because of the following action by the system when an interrupt occurs. Using the contents of sp at the time of the interrupt, the contents of sp|18 is used to find the top of the stack. Then 32 is added to this value (in case the interrupt occurred during preparation of the next frame) to obtain a pointer to free space in the stack which may be used by the system. The save sequence is also designed to execute without causing a fault when the base <u>sb</u> is locked.

Return

The standard return sequence is:

1db	sp 16,*
lreg	sp 8
rtd	sp 20

MULTICS SYSTEM-PROGRAMMERS' MANUAL SECTION BD.7.02 PAGE 7

It is possible to return control to some point other than the location immediately following the call, such a return is called an "abnormal" return. The location to be returned to abnormally, <u>abnrtn</u>, is specified by label data (see BB.2) and may be transmitted as an argument or as the contents of an external (global) location. Since abnormal returns may imply ring crossings, the abnormal return sequence is actually a system procedure (described in BD.9.05). To effect an abnormal return,

call unwinder (abnrtn)

Notes and Comments

The call, save, and return sequences assume the standard pairing of the eight base registers; i.e., $ab \leftarrow ap$, $bb \leftarrow bp$, $lb \leftarrow lp$, and $sb \leftarrow sp$. They further assume that at least $sb \leftarrow sp$ has the same value that it did when the procedure was entered. Once a procedure has been entered, $bb \leftarrow bp$ may be used freely. $lb \leftarrow lp$ must be preserved for the standard linkage machinery to work. It is extremely dangerous to change the contents of $lb \leftarrow lp$. The base sb will be locked and its contents cannot be changed by the user. It is essentially disastrous to change the contents of sp. The user may generate argument lists by any method that he chooses; however, the following suggestions constitute a rather simple method. The simplest way to generate a pointer to the location <u>place</u> is:

eapbp place stpbp pointer

<u>pointer</u> must be an even location. This method works for all types of addresses except when <u>place</u> = ap|2*i, which generates a pointer to a pointer. To avoid this, use the sequence,

ldaq ap/2*i staq pointer

which moves the ith argument pointer. When <u>place</u> is an external reference such as <seg>[e, or <seg>[[x], normal reference to the linkage section, i.e., lp[k,* (see BD.7.01 for details), will cause linking to occur at the time the pointer is generated if the sequence.

eapbp lp|k,* stpbp pointer

PAGE 8

is used. The sequence

eapbp	lp k
stpbp	pointer
lda	16,d1
orsa	pointer+1

will generate a pointer, containing an indirect modifier, to the linkage section entry at lp[k, i.e., a pointer of the form:

(1b)	its
(1p+k)	*

where (1b) is the segment number in the external base 1b and (1p) is the contents of the internal base 1p. Since indirect modifiers may or may not occur in the <u>its</u> pointers of the argument list, the called routine must assume that they do. The safe way to obtain the value of ith argument is:

lda ap/2*i,*

if it is a single word scalar, or

1 dag ap | 2*i, *

if it is a double-word scalar.

eapbp ap 2*i,*

loads $bb \leftarrow bp$ with the address of the ith argument. If the argument is an array or string, the accessing is more involved since the argument list entries point to specifiers (see BB.2). It should be noted that the call, save, and return sequences use only the eight base registers. Hence, <u>all</u> registers are preserved across the call in the caller. Further, all registers except for the bases, are preserved in the transition from calling program to called program.

EPLBSA Notes

The bootstrap assembler contains the following built-in macros.

MULTICS SYSTEM-PROGRAMMERS' MANUAL SECTION BD.7.02 PAGE 9

i) call entrypoint

ii) call entrypoint (arglist)

- iii) save
- iv) return

These macros expand into the standard sequences described above. In case (i), no argument list, the assembler substitutes eapap = 0 for eapap arglist in the call sequence given above. In EPLBSA writing,

eapbp <seg>[[ext]]

will always cause

eapbp lp|k,*

to be generated. $\frac{1}{2}$ Writing the pseudo-operation,

link k,<seg>[[x]

will define the symbol k to be equal to the relative location in the linkage section of the link for $\langle seg \rangle | [x]$. Now the user can write,

eapbp	lp/k
stpbp	pointer
lda	16,d1
orsa	pointer+1

which will generate a pointer (containing an indirect modifier) to the link for <seg>[[x] without causing linking.