

ct.list 09/26/73 1623.3 edt Wed

2014 COMPILATION LISTING OF SEGMENT ct
 Compiled by: Multics PL/I Compiler, Version II of 20 August 1973.
 Compiled on: 09/26/73 1617.3 edt wed
 Options: profile map

6.845

```

1   clock_test:
2     procedure;
3       declare
4         cv_dcc_ external entry(char(*)) returns(fixed bin(35)),
5         cv_start_ptr external entry(fixed bin,ptr, fixed bin, fixed bin),
6         clock_ external entry returns(fixed bin(71)),
7         iont_external entry options(variable),
8         (time1,time2) fixed bin(71),
9         (dt,index,limit,code) fixed bin(35),
10        arg1 fixed bin,
11        arg2 fixed bin,
12        arg3 char(arglen) based;
13
14       declare microsecond(1000) fixed bin(35);
15
16       do index = 1 to 1000;
17   end;

18
19       call cv_start_ptr(largptr,argptr,arglen,code);
20       if code = 0 then return;
21       limit = cv_dcc_(argptr,arglen);

22       do index = 1 to limit;
23         time1 = clock_();
24         time2 = clock_();
25         dt = time2 - time1;
26         if dt > 1000 then dt = 1000;
27         microsecond(dt) = microsecond(dt) + 1;
28
29   end;

30
31       do index = 1 to 1000;
32       if microsecond(index) > 0 then call iont_time(1000, index, microsecond(index));
33   end;
34
35   return;

```

clock_test 10##2000

70	53
71	921
72	736
73	186
74	57
75	8
76	8
77	4
78	2
82	1
83	3
84	2
86	2
87	1
105	1
106	2
109	2
125	1
126	1
127	1
128	1
203	2
204	2
206	2
207	2
209	1
211	1
212	3
213	2
215	1
217	3
218	1
219	2
221	2
251	1
1000	2

r 1013 .956 .200 10

with 2 cpus

```

ct$clock_test 2000
 73 1990
 74 229
 75 11
 123 1
 125 1
 126 1
1000 1
r 1131 .823 4.034 70

```

} with 1 cpu

```
>dd>ex1>print_profile ct
```

LINE	STH	COUNT	COST	PROGRAM
15	1	1	5	ct
16	1	1000	2000	
17	1	1000	5000	
19	1	1	1 + 1 (call_ext_out)	
20	1	1	2	
21	1	1	19 + 1 (call_ext_out_desc)	
23	1	1	7	
24	1	2000	12000 + 2000 (call_ext_out)	
25	1	2000	12000 + 2000 (call_ext_out)	
26	1	2000	6000	
27	1	2000	8000	
28	1	2000	10000	
29	1	2000	10000	
31	1	1	5	
32	1	1000	20000 + 1000 (call_ext_out_desc)	
33	1	1000	5000	
35	1	1	0 + 1 (return)	
TOTAL			90052 + 5003	
r 1133	.847	3.720	77	

clock_test:

procedure;

```

declare cv_dec_ external entry(char(*)) returns(fixed bin(35)),
cu_$args_ptr external entry(fixed bin,ptr,fixed bin,fixed bin(71),
clock_ external entry returns(fixed bin(71)),
ioa_ external entry options(variable),
(timel,time2) fixed bin(71),
(dt,index,limit,code) fixed bin(35),
arglen fixed bin,
argptr ptr,
arg char(arglen) based;

declare microsecond(2000) fixed bin(35);

declare l process_usage,
2 number_wanted fixed bin,
2 number_returned fixed bin,
2 cpu_time_used fixed bin(71),
2 memory_usage fixed bin(71),
2 number_of_page_faults fixed bin(35),
2 num_bulk_store_pf fixed bin(35),
2 process_virtual_time fixed bin(71),
2 x_paging_measure fixed bin (71),

hcs_fget_process_usage external entry(ptr, fixed bin(35)),
puptr pointer,
pvt(10) fixed bin(71),
position fixed bin,
addr builtin;

do index = 1 to 2000;
microsecond(index) = 0;
end;

puptr = addr(process_usage);
process_usage.number_wanted = 0;
call cu_$args_ptr(l,argptr,arglen,code);
if code ≠ 0 then return;
limit = cv_dec_(argptr->arg);

do index = 1 to limit;
position = 1;
call hcs_fget_process_usage(puptr,code);
pvt(position) = process_usage.process_virtual_time;
position = position + 1;
call hcs_fget_process_usage(puptr,code);
pvt(position) = process_usage.process_virtual_time;
dt = pvt(2) - pvt(1);
if dt > 2000 then dt = 2000;
microsecond(dt) = microsecond(dt) + 1;
end;

do index = 1 to 2000;
if microsecond(index) > 0 then call ioa_("7hd 7hd",index,microsecond(index));
end;

```

```

k_test: procedure;

declare cv_dec_external entry(char(*)) returns(fixed bin(35)),
cu_args_ptr external entry(fixed bin,ptr,fixed bin,fixed bin(35))
clock_external entry returns(fixed bin(71)),
ioa_external entry options(variable),
(timel,timr2) fixed bin(71),
(dt,index,limit,code) fixed bin(35),
arglen fixed bin,
argptr ptr,
arg char(arglen) based;

declare microsecond(1000) fixed bin(35);

declare 1 process_usage,
2 number_wanted fixed bin,
2 number_returned fixed bin,
2 cpu_time_used fixed bin(71),
2 memory_usage fixed bin(71),
2 number_of_page_faults fixed bin(35),
2 num_full_store_pf fixed bin(35),
2 process_virtual_time fixed bin(71),
2 x_paging_measure fixed bin(71),

hcs_fget_process_usage external entry(ptr, fixed bin(35)),
puptr pointer,
pvt(10) fixed bin(71),
position fixed bin,
addr builtin;

do index = 1 to 1000;
microsecond(index) = 0;
end;

puptr = addr(process_usage);
position = 1;
process_usage.number_wanted = 0;
call hcs_fget_process_usage(puptr,code);
pvt(position) = process_usage.process_virtual_time;
position = position + 1;

call cu_args_ptr(1,argptr,arglen,code);
if code ≠ 0 then return;
limit = cv_dec_(argptr->arg);

call hcs_fget_process_usage(puptr,code);
pvt(position) = process_usage.process_virtual_time;
position = position + 1;

do index = 1 to limit;
timel = clock();
time2 = clock();
dt = time2 - timel;
if dt > 1000 then dt = 1000;
microsecond(dt) = microsecond(dt) + 1;
end;

call hcs_fget_process_usage(puptr,code);

```

```
pvt(position) = process_usage,process_virtual_time;
position = position + 1;

do index = 1 to 1000;
if microsecond(index) > 0 then call ice_("7d", "7d", index,microsecond(index));
end;

call bcs_fget_process_usage(puptr,code);
pvt(position) = process_usage,process_virtual_time;

do index = 2 to position;
dt = pvt(index) - pvt(index-1);
call ice_("to position 7d, 7d microsec.",index,dt);
end;

return;
```

121 .923 .750 43

P. 206

```

clock_test 1000
70      0
71      429
72      408
73      94
74      29
75      12
76      6
106     1
107     2
108     1
109     5
121     1
122     1
126     7
127     1
190     1
203     1
211     1
214     1
219     1
222     1
223     7
224     1
226     1
232     1
1000    1
r 1620  .787  1.050  33

```

```

c1# pvt_test$clock_test 1000
703     533
704     330
705     2
706     2
707     2
708     19
709     17
710     10
711     11
712     12
713     7
714     2
715     2
717     1
719     1
721     1
722     1
723     1
727     1
735     1
738     1

```

```

740     2
741     1
743     2
744     1
745     1
746     1
747     1
748     2
749     1
750     1
751     1
753     2
754     1
756     1
761     1
762     1
769     1
777     1
785     1
786     1
787     1
788     1
789     1
790     1
792     1
795     1
796     1
797     1
798     1
799     1
800     1
801     1
802     1
803     1
804     1
805     1
806     1
807     1
808     1
809     1
810     1
811     1
812     1
813     1
814     1
815     1
816     1
817     1
818     1
819     1
820     1
821     1
822     1
823     1
824     1
825     1
826     1
827     1
828     1
829     1
830     1
831     1
832     1
833     1
834     1
835     1
836     1
837     1
838     1
839     1
840     1
841     1
842     1
843     1
844     1
845     1
846     1
847     1
848     1
849     1
850     1
851     1
852     1
853     1
854     1
855     1
856     1
857     1
858     1
859     1
860     1
861     1
862     1
863     1
864     1
865     1
866     1
867     1
868     1
869     1
870     1
871     1
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873     1
874     1
875     1
876     1
877     1
878     1
879     1
880     1
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883     1
884     1
885     1
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889     1
890     1
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895     1
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899     1
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904     1
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913     1
914     1
915     1
916     1
917     1
918     1
919     1
920     1
921     1
922     1
923     1
924     1
925     1
926     1
927     1
928     1
929     1
930     1
931     1
932     1
933     1
934     1
935     1
936     1
937     1
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940     1
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942     1
943     1
944     1
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970     1
971     1
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973     1
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978     1
979     1
980     1
981     1
982     1
983     1
984     1
985     1
986     1
987     1
988     1
989     1
990     1
991     1
992     1
993     1
994     1
995     1
996     1
997     1
998     1
999     1
r 1631  2.374  .789  27

```

```

clock_test_timed$clock_test 1000
71      178
72      737
73      90
74      5
75      2
to position 2, 1780 microsec.
to position 3, 167392 microsec.
to position 4, 193910 microsec.
r 1632  .420  .600  23

```

pcd cpu

cpu	a	7
cpu	b	6

r 1633 .472 .719 56

Real 7

clock_test_timed\$clock_test 1000

71 2
72 510
73 365
74 112
75 10
215 1

to position 2. 41470 microsec.
to position 3. 185244 microsec.
to position 4. 147290 microsec.
r 1622 .574 .998 25

clock_test_timed\$clock_test 2000

71 8
72 522
73 786
74 416
75 190
76 57
77 8
78 7
80 1
81 1
82 1
110 1
123 1
125 2
127 1
129 3
131 1
201 1
203 1
204 1
207 1
212 1
216 1
957 1
1000 7

to position 2. 2150 microsec.
to position 3. 348130 microsec.
to position 4. 390880 microsec.
r 1622 .877 .904 27

pvt_test\$clock_test1# 1000

706 1
707 2
708 35
709 52
710 59
711 44
712 50
713 22
714 18
715 9
716 9
717 7
718 5
719 2
721 1
722 4
723 4
724 1
725 2
726 1
727 2
728 2
729 5
730 1
731 2
732 2
733 2
734 3
735 3
736 3
737 1
738 6
739 6
740 1
741 2
742 6
743 3
744 10
745 22
746 44
747 64
748 63
749 62
750 46
751 49
752 52
753 23
754 17
755 10
756 3
757 5
758 5
759 2
760 6

2 CPU'S -

701	2	902	2
702	2	903	3
704	2	904	2
706	2	905	1
707	2	906	1
709	2	907	1
770	1	908	1
772	1	910	1
774	3	911	3
775	2	912	1
776	2	913	3
777	1	914	1
778	1	915	2
781	2	917	2
782	2	919	1
784	1	920	4
787	2	921	3
788	1	922	3
789	1	923	4
791	1	924	1
793	2	925	2
796	2	927	3
797	1	928	1
803	1	930	1
806	1	932	3
809	1	934	1
811	1	935	2
815	1	936	1
819	2	940	1
820	2	942	2
825	1	944	1
826	1	946	1
827	2	947	1
834	1	949	1
860	1	950	1
861	1	953	1
862	1	954	1
863	1	964	1
864	1	968	1
867	1	973	1
870	1	980	1
871	1	1023	1
874	1	1024	1
876	2	1090	1
877	1	1108	1
878	2	2000	2
879	2	r 1095	4
880	2	4,062	4
886	1	506	4
887	2		
889	1		
891	2		
892	3		
893	1		
895	1		
897	1		
898	1		
900	1		