

RECEIVED

MAY 19 1976

J. H. SALTZER

FORUM LIST

D. Clark

C. Clingen

R. Daley

J. Gintell

C. Kessler

G. Lundberg

J. Regester

**Prof. J. Saltzer**

# Honeywell

TO: List of Attendees cc: List

SUBJECT: Forum Minutes for May 5, 1976

ATTENDEES: G. Ackerman-Lewis P. B. Kelley R. Steinberg  
W. Burner E. D. Kleinow  
R. J. Chevalier R. A. Roach  
W. J. Flaherty L. J. Ryan

---

## HARDWARE - E. D. Kleinow

### Processors

- . FCO's installed. No FCO's outstanding on processors as of 5-5-76.
- . SCU-A - St. B parity error. FED spared bit and corrected problem.
- . SCU-A - St. A parity occurred but no failure found by FED.
- . SCU-B - St. A parity error. FED found low margin and is making adjustments.
- . Cache - Processor-B cache errors.  
FED replaced failing board and located faulty chip.  
Problem corrected.
- . Intermittent cache errors also reported in Processor A.  
No re-occurrence however.

### MPC500

Intermittent firmware loss.  
FED has found one failure but has not corrected the problem.  
FED still working on this.

### MPC190B

Firmware failure. FED located and replaced failing chip.

### PRT201

Unit down 1½ days.  
FED replaced new brake assembly and failing board.

HARDWARE - continued

DN355

Problems reported with the auto call unit but at this time appear to be modem related.

SOFTWARE - P. B. Kelley

28.4E installed and running.

28.3 had 3 crashes related to bug in tty\_read.

28.4A Signalling bug in Initializer process.

28.4D Syserr logging problems (Fixed in 28.4E).

28.4E has problem in dynamic reconfiguration of memory.

BILLERICA

B. Flaherty wants to have boards removed (from the DN355) for return to Billerica.

Leo says it's okay, just coordinate with FED and Operations before pulling them.

A handwritten signature in black ink, appearing to be the initials 'PB' or similar, located at the bottom left of the page.