

Status report

for NSA 4/1/70

- Multics program
- Computing at M.I.T. in 1970

I. Multics history

Started in 1964 as follow-on to 7094 CTSS

Joint project BTL/GE/MIT.

Chose GE 635 hardware to modify (GE agreed to help)

Hardware design complete in 1965 - "645" computer

645 delivered fall 1966

First working segmented environment June 1967

First self-booting supervisor Dec. 1967

First scheduled daily operation Sept. 68

24-hour/day operation May 69

Public operation at M.I.T. Oct. 69

II Objectives

Controlled access, multiple interactive access

2. controlled sharing of information.

Broad goal: Computer utilities good for a wide range of jobs.

Benefit: very complex supervisor

sophisticated protection facilities

hierarchical file system.

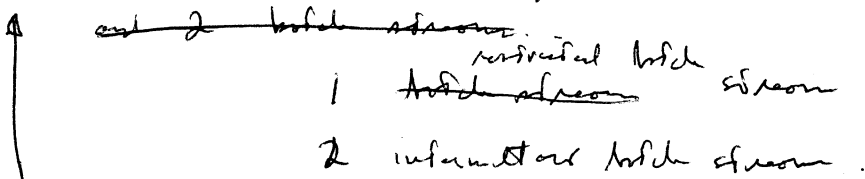
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Current Station

(check printer and definition)

operation of M.D.T. 1 CPU / 256K core service +
providing substantial revenue, but not yet fully loaded.
1 CPU / 128K core development

today total of 35 users inc. 12 system programmers



calculated in uncertain: probably ~40.

Operates with 384K core on } (not both - busy in one CPU)
2 CPUs

on Friday's

About 300 registered users outside system prog. staff
and 50 projects

During day 35 users from 12⁰⁰ → 4⁰⁰ pm.
evening 20-25 users till 12 pm.

Pattern of usage similar to early CTS 5

(Project TIP, maybe #0M105)

Uses: 2 or 3 administrative / record keeping systems
Free # student use (limited BASIC system - will have
BASIC ~ 1 yr.)

Undergraduate Comp Science class 6.231 ~ 120 students.

Project MBE research (2-8 groups)

BTL misc ; GE misc

Many misc. small projects.

Reliability

MTBF slowly climbing; about 7 hrs today.

LTBF ~ 70-80 hrs.

MTB hardware failure ~ 7 hrs.

MTB software failure ~ 60 hrs.

Reasons for hardware trouble

- grounding issue (most trouble 9-5, M-F)
- barely adequate hardware reliability aids (655 to 2078 work hours out)
- Skidage of FE's for large, strange configurations
- FE not familiar with hardware service requirements

Expect to get it under control.

Pattern similar to early CTSS

Software is currently too sensitive to hardware failures. (being fixed)

File storage reliability very high despite MTBF.

locking tapes

storage

thus: each course ~~to~~ 20-30 min. delay, but all storage is intact usually.

Language

1. PL/I on advanced computer - good code, easy to use
2 sec. + 1000 instructions/min. GE provided
2. FORTRAN ~~main~~ solid, standard
3. BASIC borrowed, seems to work ok.
4. (Coming) APL Popular - seems to be a fast translator/interpreter. GE writing it.

Others are being in others, e.g., BCPL, GASP, AFD maybe,
may be in SIMSCRIPT.

Future

Planning next hardware now.

- order to be placed w/ yr delay ~ 3 yrs. (IPC)
- based on 655 Technology
- ~~no~~ no major architectural changes
- objective: complete design in ~~1970~~ ¹⁹⁷⁰ Technology.

Continuing development of systems

performance →	~ 55 msec	1 CPU / 236K	} target
	90 "	2 CPUs / 384K	
	(no buffer)		on present hardware

Improvements to come from

1. File system needs re-code
2. Pre-merge / post merge strategy
3. Multitask file strategy
4. Better type writer control
- to 55 5. Reduced size of wind-down supervisor (~85K today)
- to 90 6. Increasing of 2nd CPU + 128K memory

Additional features

Tie to ANSA web

Complex (ARMS, net, POP-8, etc.)

More console variants - TV300, M35, etc.

user beginning to develop I/O packages, etc.

Better user control

New assembler

Other changes

simplicity

portability

ease of maintenance

continuous operation / reconfiguration

cut over to PL/I

Computing at M.I.T.

Machines in use at IPC.

- 7044 - CTSS - 20 users (planning done - announced end of 12/70.)
- 360/65 OS/360 Major batch processing load for M.I.T.
 - 2780 + 1130 remote batch
 - 360/40 used in ASP (in HASP)
- 360/67 CP/67 Used primarily to act as a virtual machine generator to allow checkout of subsystems to be delayed elsewhere. (All development, no production, since funny financing.)
 - ~16 partitions to estimate
 - 1 partition runs APC = 5 users
 - Some run CMS - an early CTSS imitator (no file sharing between users)
- GE 645 Multics Service Primary time sharing
 - Multics development } computer framework.

Other machines

- L.H. 360/67 CP/CMS exclusively
- I.L. 360/75/75/70 number crunching
- L.N.S. 360/65 Underutilized for little cluster work
- OIS 360/30 (40?) MIT. accounting, etc.

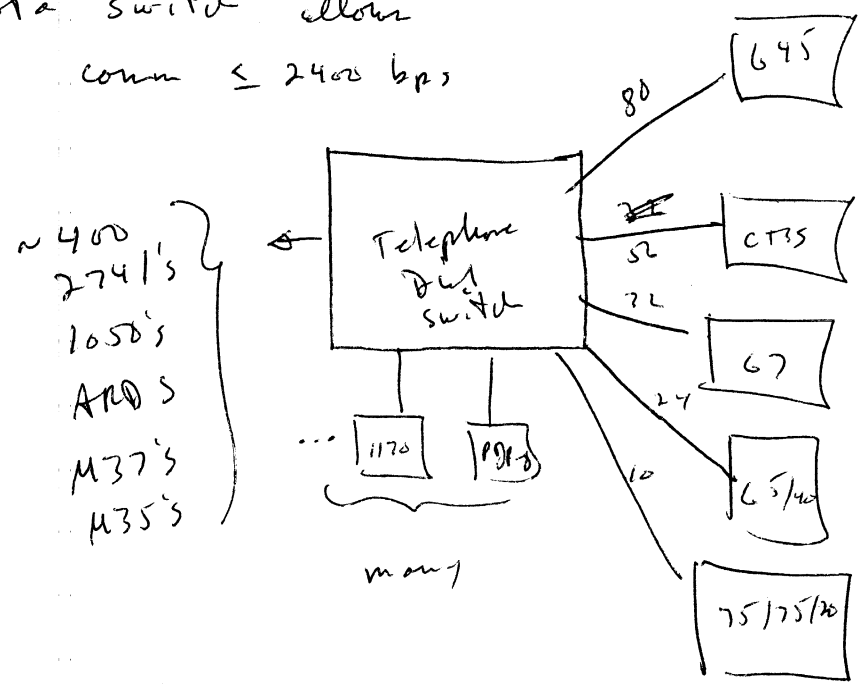
EE Dept	PDP-1	Time sharing	} experimental + their work
	TXO		

MTC	PDP 6/10	AI
	PDP 6/10	Dynamic modeling / graphics

+ Dozens of

1130's	} often used as intelligent Terminals for big machines
PDP-8's, 9's, 7's	
misc small computers	

Data switch allows
conn ≤ 2400 bps



Some special lines of 50 kbps.