

TO: John Gintell  
FROM: John P. Zona  
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SUBJECT: PRESENT HARDWARE CONFIGURATION

FJ Corbato  
AC Daley  
JH Saltzer ✓  
JM Grochow ✓

The following list is memory oriented. There are eight ports on each CPU memory and GIOC. The ports available column means that the logic is in the machine for that many ports.

*currently required changes are marked here*

<u>MAJOR UNIT</u>	<u>MODEL NUMBER</u>	<u>SERIAL NUMBER</u>	<u>PORTS AVAILABLE</u>	<u>PORTS USED</u>	<u>PORT USE</u>
CPU-A	CP8031A1	2	8	5	CLK, MEMS E, F, G, H
CPU-B	CP8031B1	9	4 + 1	4 + 1	CLK, MEMS F, G, H <i>needs 1 for Mem E</i>
MEM G INTERNAL (FAB)	MM8040AA1	4	5 + 1	5 + 1	CPU A, B GIOC A, B F. H. DRUM <i>needs 1 for 2nd F.H. Drum</i>
MEM G EXTERNAL (AMPEX)	AUM600AA1	6			
CLOCK (Part of EXT MEM G)	CLK600AA1	5			
MEM H INTERNAL (FAB)	MM8030B2	56	8	5	CPU A, B GIOC A, B F. H. DRUM
MEM H EXTERNAL (AMPEX)	AM8030A1	39			
MEM F INTERNAL (FAB)	MM8030B2	79	7	5	CPU A, B GIOC A, B F. H. DRUM
MEM F EXTERNAL (AMPEX)	AUM600AA1	7			
PROTO CLOCK	CAB601AA1X2	7	2	2	CPU A, B
MEM E (FAB)	MM8030B1	6	8	5	CPU A, B GIOC A, B F. H. DRUM
FIREHOSE CONTROLLER	EMC302B	4	4	3 + 1	MEMS F, G, H + E

(Continued)

<u>MAJOR UNIT</u>	<u>MODEL NUMBER</u>	<u>SERIAL NUMBER</u>	<u>PORTS AVAIL</u>	<u>PORTS USED</u>	<u>PORT USE</u>
FIREHOSE DRUM	EMU302A1	8		(Cabled to Above controller)	
FIREHOSE CONTROLLER	EMC302B	5	4	: 1	MEM E + F, G, H
FIREHOSE DRUM	EMU302A1	9		(Cabled to above controller)	
G IOC A (MOD B)	DC8031B1	8	4	4 =	MEMS E, F, G, H
G IOC B (MOD A)	DC8031A1	1	4	4	MEMS E, F, G, H

This list is adapter oriented. Each GIOC could take one more adapter cabinet. Each adapter cabinet has three doors, door A, B, and C. Door A and C have room for 10 rows of adapter logic and door B has room for 7 rows of adapter logic. When adapters are ordered the rows of logic that are contained in the desired adapter and whether it is to be placed in a LH door (door C), RH door (door A) or middle door (door B), has to be specified.

GIOC CABINET DOOR ADAPTER SERIAL LOCIC CHANNELS CHANNELS CHANNELS CHANNEL TRANSFER RATE  
 MOD TYPE NUMBER ROWS AVAILABLE POSSIBLE USED USE  
 OCCUPIED

MOD	TYPE	NUMBER	ROWS	AVAILABLE	POSSIBLE	USED	TRANSFER RATE
B	1	A	HPC600C1	2	5	1	1 not used 400kc (CHAR)
B	1	(A)	TTA600E1	19	5	16	32 12 37's 150 BAUD per/chan
B	1	B	CAA600G	8	3	3	3 not used 2400 Baud per/chan
B	1	B	IPA600C1	8	4	6	6 CR A,B 10kc (CHAR) PRT A,B per/chan CP A CONA
B	1	C	HPC600D1	35	5	1	1 not used 400kc (CHAR)
B	1	C	TTA600F1	21	5	16	32 16 2741's 133 BAUD per/chan
B	2	A	CSA600C1	10	3	3	2 PDP8 2400BAUD per/chan
B	2	B	DDA600C1	9	5	2	2 1 DS10 60kc CHAR
B	2	B	DGA600C1	7	2	8	8 not used n/A
B	2	C	HPC600D1	45	5	1	1 1 MTC 400kc (CHAR)
B	2	C	TTA600F1	20	5	16	32 16 2741's 133 BAUD per/chan
A	1	A	HPC600A1	1	5	1	1 not used 400kc (CHAR)
A	1	A	TTA600E1	25	5	16	32 5 2741's 133 BAUD
A	1	B	CAA600C1	10	3	3	3 not used 2400 BAUD per/chan
A	1	B	1PA600C1	5	4	6	6 CRA, B 10kc CHAR per/chan PRTA, B CP B CON B
A	1	C	HPC600B1	24	5	1	1 not used 400kc (CHAR)
A	1	(C)	TTA600F1	26	5	16.8	32 not used 2741 133 BAUD
A	2	A	CSA600C1	7	3	3	3 2 PDP8 2400 BAUD
A	2	A	HPC600A1	27	5	1	1 not used 400kc (CHAR)
A	2	B	DDA600A1	2	5	2	2 1 DS10 60kc (CHAR)
A	2	B	DGA600A1	3	2	8	8 not used NA
A	2	C	HPC600A1	1	5	1	1 1 MTC 400kc (CHAR)
A	2	C	TTA600F1	27	5	16	32 4 37 150 BAUD per/chan

How many rows unused in 8 TTC/TTT channel group wire?