MASSACHUSETTS INSTITUTE OF TECHNOLOGY

PROJECT MAC

November 4, 1966

Mr. George Futas
Computer Division, C50
General Electric Company
13430 North Black Canyon Highway
Phoenix, Arizona

Dear George:

    Enclosed are several sections from the Multics System Programmers'
Manual which describe the way in which the supervisor programs are
interfacing with the hardware of the system. In particular, there
are sections on the System Clock, Bootstrap loader,and Interrupt
and Fault handling. Any comments you might have would be appreciated.
As I recall you suggested once that you would like to keep in touch
with the way the hardware is being used.

    Although a few of the documents are in "draft" status, all of
them are "firm" in the sense that they represent the way we presently
are planning the implementation. There are several more documents
yet to be written; I will send them as appropriate.

                        Yours truly,

                        Jerome H. Saltzer
                        Assistant Professor
                        of Electrical Engineering

JHS/ssh

Enclosures:

BC.1.02	BK.1.04	BL.1.00
BC.1.03	BK.2.01	BL.1.01
BC.3.01	BK.2.02	BL.1.02
BD.10.01	BK.2.05	
BD.10.02	BK.3.01	
BD.10.03	BK.3.02	
BJ.0	BK.3.03	
BK.0	BL.0	