

Don lucders

To : B.L.G.

From : J. Saltzer

Subj : M50E Book 25, Eng. Prod. Spec., GE-645 Prototype System Clock

Dated June 13, 1966; no author

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Observations:

1. The document appears to have been done with TYPSET / RUNOFF.

p. 2-2, line 10, mis-typed sentence, meaning unknown.

p. 2-3, bottom line. Is Interrupt Cell 16 a parallel wire into the Alarm clock or in the temporary clock controller?

p. 2-5. Why doesn't the Clock Controller generate "illegal action" for slave mode  $\rightarrow$  SAC, etc.<sup>2</sup>

p. 2-5. If lower half of Alarm clock cannot be loaded, is compound disableable or will an interrupt occur?

p. 2-6 <sup>question</sup> SCLK - say, or SAC ( p. 2-5 )

p. 2-7. Can I tie up the clock in a slave mode loop?

p. 3-3 A processor may not work itself either.