1. Processor Interval timer measures memory cycles.

Calendar clock

2. Clock adapter (?) Outboard of GIOC on some channel.

a. 72 bits long.
b. initial value set by dial to include year, date, etc.
c. Ticks at 1 μsec intervals
d. When some bit (adjustable) turns over, 72-bit word is dumped into some memory cell.
(How? via arithmetic word?)

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3. Wake up clock in GIOC

Expensive

a. 72 bits long, set by program from memory.
b. continuous compare with calendar clock — if calendar ≥ wkup. generate a status word → interrupt.

the character compare—word compare mechanism

1. Processor Internal Timer measure memory cycles.

2. Time clock ticks at 1 sec. updates memory on basis of adjustable bit flipping. in GLØC.

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3. Wake-up clock 32 bit