

Glosser.

8/27/69

1. G10C evolution - what to look for

Buffer space seems large - could use a cheaper memory for it.

What % of cost is related to common peripheral compatibility?

Why was RT10C so expensive?

Does it have capacity to be used with MEX? (How about drum?)

What tools would be best to upgrade it into MEX?

2. ESS Switchgear - what to watch for in use with data.

How is getting problem of step-by-step reduction?

Where can I learn more

↳ review

3. Do ROT switches really solve the problem of step by step computer usage?

Where can I learn more.

3.5 Daily chain drive failures.

4. What is the fundamental difference between step by step and #5 computer?

5. I still have problem on GUC TTY demands. (Maybe  
mapping, but it is very difficult)

6. W are document - very impressive (Any plan to  
get together again?)

7. Union 1108

## 6-10c / Glasser

1. Absolute addressing / segment addressing would require.
2. Common peripheral package should happen.
3. 1 Interrupt queue is enough.
4. Extra control DCU's are not needed.
5. Simpler pinout structure.
6. Should have 3 channel types. 

Input only	(also Interrupt)
Output only	(Lowcost)
DIO	
7. Could use absolute addressing inside, but use page tables.
8. Could put buffering on TTY line offboard.  
Should have