Bibliography

- C. Scott Ananian, Krste Asanović, Bradley C. Kuszmaul, Charles E. Leiserson, and Sean Lie. Unbounded transactional memory. In *Proceedings of the 11th International Symposium on High-Performance Computer Architecture (HPCA'05)*, pages 316–327, San Franscisco, California, February 2005.
- [2] Andrew W. Appel and Kai Li. Virtual memory primitives for user programs. In ASPLOS-IV, pages 96–107, April 1991.
- [3] Lars Arge and Jeffrey Scott Vitter. Optimal dynamic interval management in external memory. In FOCS 1996, pages 560–569, October 1996.
- [4] M. A. Bender, E. Demaine, and M. Farach-Colton. Cache-oblivious B-trees. In FOCS 2000, pages 399–409, 2000.
- [5] Philip A. Bernstein and Nathan Goodman. Multiversion concurrency control—theory and algorithms. ACM Transactions on Database Systems (TODS), 8(4):465–483, 1983.
- [6] Philip A. Bernstein, Vassos Hadzilacos, and Nathan Goodman. Concurrency Control and Recovery in Database Systems. Addison-Wesley, 1987.
- [7] Brian N. Bershad, Craig Chambers, Susan J. Eggers, Chris Maeda, Dylan McNamee, Przemyslaw Pardyak, Stefan Savage, and Emin Gun Sirer. SPIN – an extensible microkernel for application-specific operating system services. In ACM SIGOPS European Workshop, pages 68–71, 1994.
- [8] Haran Boral, William Alexander, Larry Clay, George Copeland, Scott Danforth, Michael Franklin, Brian Hart, Marc Smith, and Patrick Valduriez. Prototyping Bubba, a highly parallel database system. *IEEE Transactions on Knowledge and Data Engineering*, 2(1):4–24, 1990.
- [9] Peter A. Buhr and Anil K. Goel. uDatabase annotated reference manual, version 1.0. Technical report, Dept. of Comp. Sci., Univ. of Waterloo, Ontario, Canada, September 1998.
- [10] Rémy Card, Eric Dumas, and Franck Mével. The Linux Kernel Book. John Wiley and Sons, 1999.
- [11] Albert Chang and Mark F. Mergen. 801 storage: Architecture and programming. ACM Transactions on Computer Systems, 6(1):28–50, February 1988.
- [12] J. M. Cheng, C. R. Loosely, A. Shibamiya, and P. S. Worthington. IBM Database 2 performance: Design, implementation and tuning. *IBM Systems J.*, 23(2):189–210, 1984.
- [13] W. P. Cockshot, M. P. Atkinson, K. J. Chisholm, P. J. Bailey, and R. Morrison. Persistent object management system. *Software-Practice and Experience*, 14(1), January 1984.
- [14] Keir Fraser. Practical lock-freedom. Technical Report 579, University of Cambridge, February 2004.

- [15] Matteo Frigo. The weakest reasonable memory model. Master's thesis, MIT Department of Electrical Engineering and Computer Science, January 1998.
- [16] Matteo Frigo. Portable High-Performance Programs. PhD thesis, MIT EECS, June 1999.
- [17] R. Goldberg and R. Hassinger. The double paging anomaly. In Proc. 1974 National Computer Conference, pages 195–199, May 1974.
- [18] Jim Gray. The transaction concept: Virtues and limitations. In Seventh International Conference of Very Large Data Bases, pages 144–154, September 1981.
- [19] Jim Gray, Paul McJones, Mike Blasgen, Bruce Lindsay, Raymond Lorie, Tom Price, Franco Putzolu, and Irving Traiger. The recovery manager of the System R database manager. ACM Computing Surveys, 13(2):223–242, 1981.
- [20] Jim Gray and Andreas Reuter. *Transaction Processing: Concepts and Techniques*. Morgan Kaufmann, 1993.
- [21] Lance Hammond, Brian D. Carlstrom, Vicky Wong, Ben Hertzberg, Mike Chen, Christos Kozyrakis, and Kunle Olukotun. Programming with transactional coherence and consistency (TCC). In ASPLOS-XI: Proceedings of the 11th International Conference on Architectural Support for Programming languages and Operating Systems, pages 1–13, New York, NY, USA, 2004. ACM Press.
- [22] Lance Hammond, Vicky Wong, Mike Chen, Brian D. Carlstrom, John D. Davis, Ben Hertzberg, Manohar K. Prabhu, Honggo Wijaya, Christos Kozyrakis, and Kunle Olukotun. Transactional memory coherence and consistency. In *ISCA '04: Proceedings of the 31st Annual International Symposium on Computer Architecture*, page 102, Washington, DC, USA, 2004. IEEE Computer Society.
- [23] Tim Harris and Keir Fraser. Language support for lightweight transactions. In OOPSLA '03: Proceedings of the 18th Annual ACM SIGPLAN Conference on Object-Oriented Programming, Systems, Languages, and Applications, pages 388–402, New York, NY, USA, 2003. ACM Press.
- [24] M. Herlihy and J. E. B. Moss. Transactional memory: Architectural support for lock-free data structures. In Proceedings of the Twentieth Annual International Symposium on Computer Architecture, 1993.
- [25] Maurice Herlihy, Victor Luchangco, Mark Moir, and III William N. Scherer. Software transactional memory for dynamic-sized data structures. In PODC '03: Proceedings of the Twenty-Second Annual Symposium on Principles of Distributed Computing, pages 92–101, New York, NY, USA, 2003. ACM Press.
- [26] Maurice P. Herlihy, Victor Luchangco, and Mark Moir. Obstruction-free synchronization: Double-ended queues as an example. In *ICDCS*, pages 522–529, Providence, Rhode Island, May 2003.
- [27] Maurice P. Herlihy and Jeannette M. Wing. Linearizability: a correctness condition for concurrent objects. ACM Transactions on Programming Languages and Systems (TOPLAS), 12(3):463–492, 1990.
- [28] Shigekazu Inohara, Yoji Shigehata, Keitaro Uehara, Hajime Miyazawa, Kouhei Yamamoto, and Takashi Masuda. Page-based optimistic concurrency control for memory-mapped persistent object systems. In *HICSS (2)*, pages 645–654, 1995.
- [29] Zardosht Kasheff. Cache-oblivious dynamic search trees. Master's thesis, MIT EECS, June 2004.
- [30] Kevin Poulsen. Tracking the blackout bug. http://www.securityfocus.com, 2004.

- [31] T. Kilburn, D. B. G. Edwards, M. J. Lanigan, and F. H. Summer. One-level storage system. *IRE Trans. on Electronic Computers*, EC-11(2):223–235, April 1962.
- [32] H.T. Kung and John T. Robinson. On optimistic methods for concurrency control. ACM Transactions on Database Systems, 6(2):213–226, June 1981.
- [33] B. W. Lampson and H. E. Sturgis. Crash recovery in a distributed data storage system. Technical report, Xerox PARC, April 1979.
- [34] Victor Luchangco. Memory Consistency Models for High Performance Distributed Computing. PhD thesis, MIT, 2001.
- [35] Dylan James McNamee. Virtual Memory Alternatives for Transaction Buffer Management in a Single-level Store. PhD thesis, Univ. of Wash., 1996.
- [36] C. Mohan, Don Haderle, Bruce Lindsay, Hamid Pirahesh, and Peter Schwarz. ARIES: a transaction recovery method supporting fine-granularity locking and partial rollbacks using write-ahead logging. ACM Trans. Database Syst., 17(1):94–162, 1992.
- [37] Elliot I. Organick. The Multics System: An Examination of its Structure. The MIT Press, Cambridge, MA, 1972.
- [38] The PostgreSQL Global Development Group. PostgreSQL 7.2.1 Documentation, 2001.
- [39] Harald Prokop. Cache-oblivious algorithms. Master's thesis, MIT EECS, June 1999.
- [40] John Rosenberg, Alan Dearle, David Hulse, Anders Lindström, and Stephen Norris. Operating system support for persistent and recoverable computations. *Commun. ACM*, 39(9):62– 69, 1996.
- [41] Yasushi Saito and Brian Bershad. A transactional memory service in an extensible operating system. In USENIX Annual Technical Conference, pages 53–64, 1998.
- [42] Nir Shavit and Dan Touitou. Software transactional memory. In Symposium on Principles of Distributed Computing, pages 204–213, 1995.
- [43] Abraham Silberschatz and Peter B. Galvin. Operating System Concepts. Addison-Wesley, Fifth edition, 1998.
- [44] Sleepycat Software. The Berkeley database. http://www.sleepycat.com, 2005.
- [45] Frank G. Soltis. Inside the AS/400. Duke Press, Loveland, Colorado, 1997.
- [46] Alfred Z. Spector, D. Thompson, R.F. Pausch, J.L. Eppinger, D. Duchamp, R. Draves, D.S. Daniels, and J.L. Bloch. Camelot: A distributed transaction facility for Mach and the Internet— An interim report. Technical Report CMU-CS-87-129, Carnegie Mellon University, 1987.
- [47] Seth J. White and David J. DeWitt. QuickStore: A high performance mapped object store. VLDB Journal: Very Large Data Bases, 4(4):629–673, 1995.