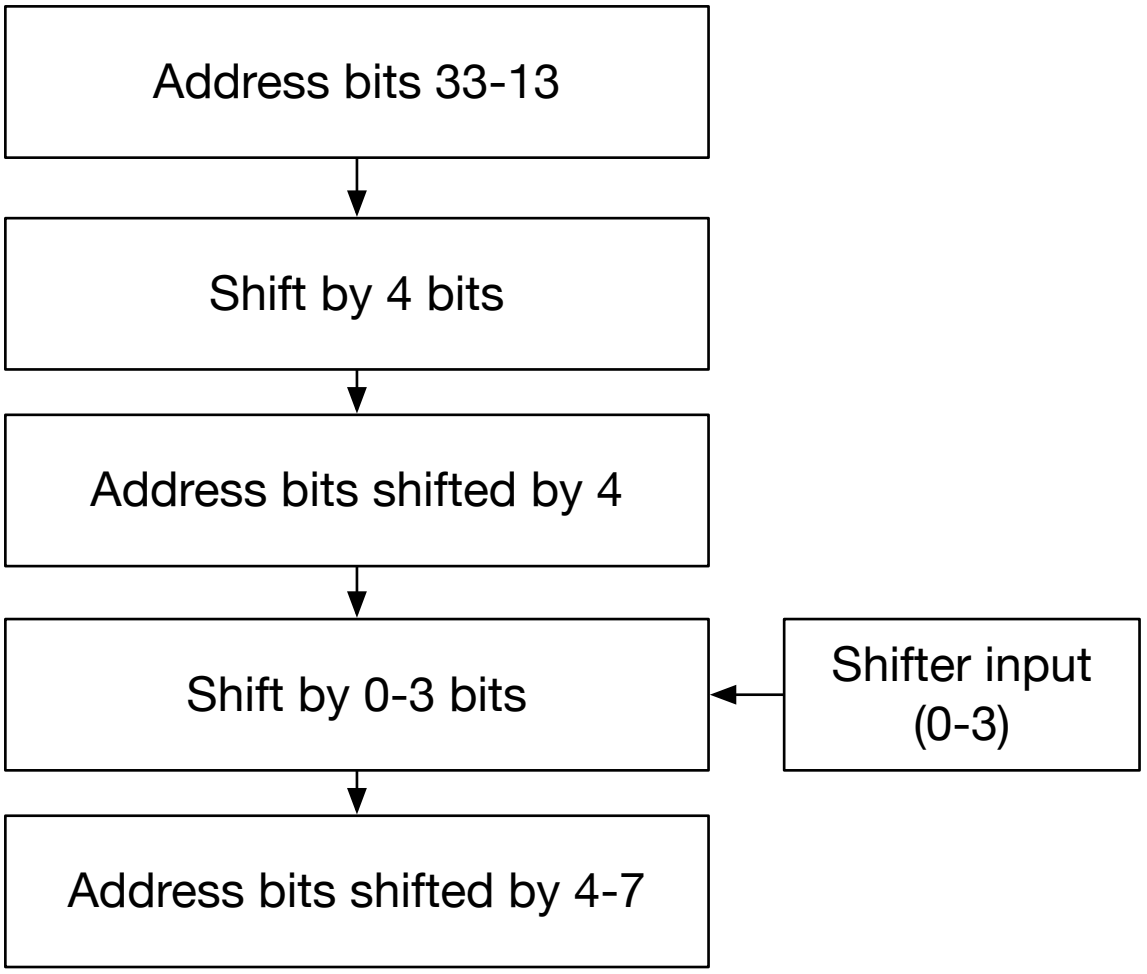
Address bits 33-13
Shift by 4 bits
Address bits shifted by 4
Shift by 0-3 bits
Shifter input
(0-3)
Address bits shifted by 4-7