

Address bits covering the maximum addressable physical space of 2 MB

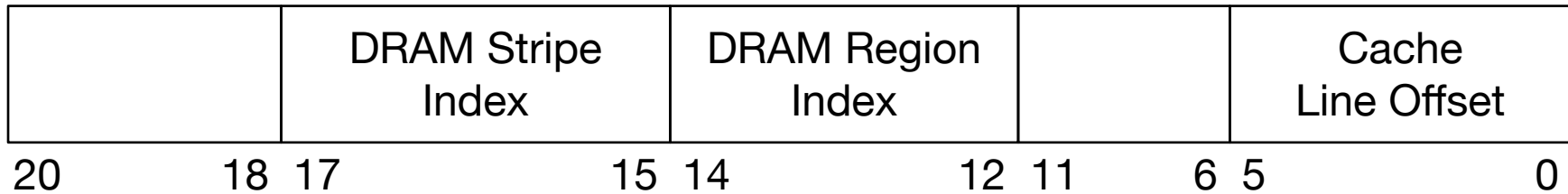
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Address bits used by 256 KB of DRAM

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Cache Set Index

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Cache Tag

Page Offset

Physical page number (PPN)