# Time-Restricted Sequence Generation* 

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#### Abstract

The classes of sequences generated by time- and space- restricted multiple counter machines are compared to the corresponding classes generated by similarly restricted multiple tape Turing machines. Special emphasis is placed on the class of sequences generable by machines which operate in real time. Real-time Turing machines are shown to be strictly more powerful than real-time counter machines. A number of questions which remain open for real-time Turing machines are settled for real-time counter machines.


## Introduction

In recent years much work has been done on the complexity of Turing machine computations. Parallelling this work on time- and space-restricted Turing machines, we consider similar restrictions on machines which, in place of tapes, have registers capable of holding integers which can be incremented or decremented by one and tested for zero. Although such counter machines (CM's) appear to be much simpler than Turing machines (TM's), unrestricted CM's have been shown by Minsky [8] to be as powerful as TM's. Counter machines, being susceptible to formal definition using little more mathematics than vector addition, are somewhat more tractable

[^0]than restricted TM's; therefore, a number of questions which are still open for restricted TM's are settled in this paper for restricted CM's.

We confine our attention in this paper to machines as sequence generators. The main theme of the paper is a comparison of the properties of time-restricted TM and CM generators. In an earlier paper [4], we have made a similar comparison of language recognizers. Since sequence generation can be viewed as recognition of a language encoded over a one letter alphabet, a number of the results in this paper strengthen results reported in our earlier work.

Special attention is paid to sequence generators which operate in real time, i.e., emit a bit at every step. The work of Hartmanis and Stearns [5] and Fischer [2] indicates that real-time generation plays a fundamental role in the study of time requirements of more general computations. Moreover, a sequence generable within any computable time restriction is, by definition (cf. Section 1.1), a homomorphic image of a real-time generable sequence. This observation yields immediate extensions to a number of results reported in this paper which, for simplicity, have been stated and proved only for real-time generators.

A surprising application of the notion of real-time generability has appeared in proofs by Cobham [1] of some special cases of

Conjecture (Hartmanis-Stearns). Let $x$ be a real number, $0 \leqslant x<1$. If the infinite binary expansion of $x$ is real-time generable by a CM, then $x$ is either rational or transcendental.

This conjecture lends yet more motivation to the study of real-time sequence generation.

## 1. Preliminaries

### 1.1. The Models

An $n$ counter machine ( $n$ - CM ) comprises a finite state control and $n$ counters, each capable of containing any integer. The control unit of an $n$-CM is partitioned into active states (which emit outputs) and dormant states (which do not emit outputs). At the start of its computation, the $n-\mathrm{CM}$ is in a designated initial state, and all counters are set to zero. Each step in a CM computation is uniquely determined by the state of the control unit and by that subset of the counters which contain zero. The action at a step consists of adding $0,+1$, or -1 independently to each counter and changing the state of the control unit. If the state of the CM is active, then an output is also emitted at that step. The sequence generated by the CM is then the sequence of outputs emitted during the course of the computation. Note that CM's operate autonomously. Thus, each CM computes a unique sequence, and each computation is infinite in that no "halting" mechanism is assumed.

More precisely, an $n$-CM $\mathbf{M}$ is specified by
(1) a finite set $Q$ of states which is partitioned into a set $Q_{a}$ of active states and a set $Q_{d}$ of dormant states;
(2) a designated initial state $q_{0} \in Q$;
(3) a machine function ${ }^{1} M$,

$$
M: Q \times\{0,1\}^{n} \rightarrow Q \times\{-1,0,1\}^{n}
$$

(4) an output function $\omega$,

$$
\omega: Q_{a} \rightarrow\{0,1\}
$$

Let $Z$ denote the set of integers. For any $z \in Z$, define

$$
\operatorname{sg}(z)= \begin{cases}0 & \text { if } z=0 \\ 1 & \text { otherwise }\end{cases}
$$

Extend the function $s g$ to $Z^{n}$ by

$$
s g\left(\left\langle x_{1}, \ldots, x_{n}\right\rangle\right)=\left\langle s g\left(x_{1}\right), \ldots, s g\left(x_{n}\right)\right\rangle
$$

For $\left\langle x_{1}, \ldots, x_{n}\right\rangle$ and $\left\langle y_{1}, \ldots, y_{n}\right\rangle$ in $Z^{n}$, define

$$
\left\langle x_{1}, \ldots, x_{n}\right\rangle+\left\langle y_{1}, \ldots, y_{n}\right\rangle=\left\langle x_{1}+y_{1}, \ldots, x_{n}+y_{n}\right\rangle
$$

A configuration of the $n$-CM M is an element of $Q \times Z^{n}$. If $C=\left(s,\left\langle x_{1}, \ldots, x_{n}\right\rangle\right)$ is a configuration of the $n-\mathrm{CM}$, then the state of $\mathbf{M}$ in configuration $C$ is $\sigma(C)=s$. The successor of configuration $C$ is the configuration

$$
\Sigma(C)=\left(s^{\prime},\left\langle y_{1}, \ldots, y_{n}\right\rangle\right)
$$

where

$$
M\left[s, \operatorname{sg}\left(\left\langle x_{1}, \ldots, x_{n}\right\rangle\right)\right]=\left(s^{\prime},\left\langle z_{1}, \ldots, z_{n}\right\rangle\right)
$$

and

$$
\left\langle y_{1}, \ldots, y_{n}\right\rangle=\left\langle x_{1}, \ldots, x_{n}\right\rangle+\left\langle z_{1}, \ldots, z_{n}\right\rangle .
$$

The computation of the $n$-CM $M$ is the (infinite) sequence of configurations $C_{0}, C_{1}, C_{2}, \ldots$ where $C_{0}=\left(q_{0},\langle 0, \ldots, 0\rangle\right)$, and for each $i>0 C_{i}=\Sigma\left(C_{i-1}\right)$. The state-sequence generated by $\mathbf{M}$ is the sequence $s_{0}, s_{1}, \ldots$ where, for each $i \geqslant 0$, $s_{i}=\sigma\left(C_{i}\right)$.

The sequence generated by $\mathbf{M}$ is the binary sequence $\alpha=a_{0}, a_{1}, a_{2}, \ldots$ where the $a_{i}$ are specified as follows:

[^1]Let $q_{0}, q_{1}, \ldots$ be the state-sequence generated by $\mathbf{M}$.
(1) Let $t(0)=\mu k\left[q_{k} \in Q_{a}\right]{ }^{2}$
(2) For each $j>0$, let

$$
t(j)=\mu k\left[k>t(j-1) \& q_{k} \in Q_{a}\right]
$$

Then, for each $j=0,1,2, \ldots, a_{j}=\omega\left(q_{t(j)}\right) .^{3}$
Let $T$ be any increasing function from the nonnegative integers into the nonnegative integers. We say that $\mathbf{M}$ operates within time $T$ if $t(k) \leqslant T(k)$ for all $k$. $\mathbf{M}$ operates in time $T$ if $t(k)=T(k)$ for all $k$.

Similar definitions can be made for Turing machines. We refer the reader to Hartmanis and Stearns [5] for details.

For each such function $T$ and positive integer $m$, we let $\mathbf{C}_{T}(m)$ denote the class of sequences generable within time $T$ by $m$-CM's, and let $\mathbf{C}_{T}=\bigcup_{m=0}^{\infty} \mathbf{C}_{T}(m)$. We let $\mathrm{T}_{T}(m)$ and $\mathrm{T}_{T}$ denote the corresponding classes for TM's.

Of particular interest is the case $T(n)=n$. Automata which operate in time $T(n)=n$ are said to operate in real time, and the sequence emitted by such an automaton is said to be real-time generable. Obviously, an automaton which operates in real time has no accessible dormant states.

Two automata are said to be time-equivalent if they generate the same sequence and operate in the same time.

### 1.2. Preliminary Results

We state, without proof, a number of results from [4] which will be useful in the sequel.
(a) Space Complexity

We say an $m$-CM ( $m$-TM) operates in space $S$ [ $S$ being a nondecreasing function from the nonnegative integers to the nonnegative integers] if $S(n)$ is the largest integer, in magnitude, assumed by any counter (respectively, is the number of squares of tape visited) until the $n$-th element of the generated sequence is emitted.

Proposition 1.1. Let $\mathbf{M}$ be an $m$-CM which operates in space $S$. Then:
(i) There is a constant c such that $\mathbf{M}$ operates within time $T(n) \leqslant c n S(n)^{m}$.
(ii) For any real $\epsilon>0$, one can find a time-equivalent $\mathbf{C M} \mathbf{M}^{\prime}$ which operates in space $\left.S^{\prime}(n) \leqslant\left[S(n)^{\epsilon}\right]\right]^{4}$
(iii) One can find an equivalent 1-TM which operates in space $S^{\prime}(n) \leqslant \log S(n)$.

[^2]
## (b) Variants of the Model

It will, on occasion, be useful to consider certain variants of our CM model. We now enumerate some variants which do not affect the operation time of a CM.

Proposition 1.2. Given an m-CM with the ability to alter the contents of each counter independently by any integer between $+c$ and $-c$ in a single step (for some fixed integer $c$ ), one can effectively find a time-equivalent (ordinary) $m$ - CM .

Proposition 1.3. Given an $m$ - CM , one can effectively find a time-equivalent $m-\mathrm{CM}$ which
(i) stores only nonnegative integers in its counters;
(ii) alters its counters at most every $c$ steps, for some integer $c>0$;
(iii) alters at most one counter per step.

For convenience we shall generally make use of Proposition 1.3(i) without explicit note.

### 1.3. Basic Necessary Conditions

In this section we develop two basic tools for showing that a sequence is not real-time generable by an $m$-CM. No comparable tools for TM's are as yet known.

Let $\alpha=a_{0}, a_{1}, a_{2}, \ldots$ be an (infinite) binary sequence. For any integer $k>0$, a $k$ slice of $\alpha$ is a subsequence of the form $a_{i}, \ldots, a_{i+k-1}$, i.e., a contiguous length $k$ subsequence.

Lemma 1.1. Let $\alpha$ be a sequence real-time generable by an $m$-CM M. There is a constant $c$ such that the number of distinct $k$ slices of $\alpha$ cannot exceed $c \cdot k^{m}$.

Proof. Say that $\mathbf{M}$ has $q$ states. We call two configurations of $\mathbf{M} k$ equivalent if they differ only in coordinates where both contain integers exceeding $k$.

Clearly, if $\mathbf{M}$ is started in either of two $k$-equivalent configurations, the next $k$ steps of the computation will be indistinguishable in terms of state sequence, hence, in terms of output sequence.

Since the number of $k$-equivalent configurations of $\mathbf{M}$ cannot exceed $q(k+2)^{m}$, the lemma readily follows.

As an immediate application of the lemma, we find

Corollary. The sequence of successive integers in binary notation is not in $\mathbf{C}_{n}$.
Obviously, the sequence has $2^{k}$ distinct $k$ slices for every integer $k$.

The condition of Lemma 1.1 is not sufficient to insure membership in $\mathbf{C}_{n}$, even for sequences which are in $\mathrm{T}_{n}$. The following useful condition was suggested by A. Cobham.

Lemma 1.2. Let $\alpha$ be a sequence in $\mathbf{C}_{n}$. Then there is a binary word $w$ such that $\alpha$ has arbitrarily long slices of the form www $\cdots$.

Proof. The proof is based on the following easily verified fact: Let $\mathbf{M}$ be an $m$-CM with the property that, for some fixed integer $k$, at every step in its computation, some counter of $\mathbf{M}$ contains an integer not exceeding $k$. Then $\mathbf{M}$ can effectively be replaced by a time-equivalent ( $m-1$ )-CM.

Therefore, let $r$ be the least integer for which there is an $r$-CM M which real-time generates $\alpha$. If $r=0$, we are done. If $r>0$, then for every integer $h$, there are configurations attained by $\mathbf{M}$ in which the contents of all counters exceed $h$; else $\mathbf{M}$ could be replaced by a real-time $(r-1)$-C.M. Once such a configuration is attain $\ldots$, $\mathbf{M}$ must exhibit the ultimately periodic behavior of a finite state machine for the next $h$ steps. Since $h$ was arbitrary, the lemma follows.

The well-known sequence of Thue [11] contains no slice of the form www; hence,

## Corollary (A. Cobham). The Thue sequence is not in $\mathbf{C}_{n}$.

This latter sequence is of special interest since it is real-time generable by a ${ }^{\top}$, and the number of distinct $k$ slices is uniformly bounded by a second degree polynomial in $k$.

## 2. Stability of Complexity Classes

In this section we consider the stability of the time complexity classes under perturbations of the timing functions, under changes in the number of counters or tapes, and under operations on the sequences.

### 2.1. Speedup Theorems

The first main distinction between CM's and T'M's is in the area of speedup. The following result follows from Hartmanis and Stearns [5] in conjunction with Fischer [2] or Meyer, Rosenberg, and Fischer [7]. ${ }^{6}$

[^3]Theorem 2.1. Let $\mathbf{M}$ be an $m$-TM which operates within time $T(n)$, and let $\epsilon>0$ be any real number such that $\epsilon \cdot T(n) \geqslant n$. One can effectively find an $(m+3)$-TM which is equivalent to $\mathbf{M}$ and which operates within time $[\epsilon \cdot T(n)]$. Thus, $\mathbf{T}_{T}(m) \subseteq \mathbf{T}_{[\epsilon T]}(m+3)$.

It is not known if the three additional tapes are essential.
Corollary. For all $c>1, \mathrm{~T}_{c n}=\mathrm{T}_{n}$. That is, "linear time" coincides with real time for TM's.

This result contrasts with the speedup theorem for CM's.
Theorem 2.2. Let $\epsilon>0$ be any real number. Given a CM M which operates within time $T(n)=n+E(n)$, one can find an equivalent $C M \mathbf{M}^{\prime}$ which operates within time $T^{\prime}(n)=n+[\epsilon E(n)]$.

Thus, $\mathbf{C}_{n+E(n)}=\mathbf{C}_{n+[\epsilon E(n)]}$.
Theorem 2.2 follows immediately from Proposition 1.3(ii).
The following result shows that Theorem 2.2 is best possible.
Theorem 2.3 (M. J. Fischer²). For any $\epsilon>0$, there is a sequence $\alpha$ in $\mathbf{C}_{(\mathbf{1}+e) n}-\mathbf{C}_{n}$.
Proof. We exhibit a sequence which satisfies the theorem. The emphasis will be on simplicity of exposition rather than on constructing the "tightest" sequence which works.

## (a) Generation of $\alpha$

The sequence $\alpha$ is constructed by emitting, in order, binary representations of the successive integers interspersed with strings of 0 's. In particular, the binary representation of each integer $m$ is preceded by a string of 0 's of length $2^{[\log m]+1}$.

We describe a $5-\mathrm{CM} \mathbf{M}$ which generates $\alpha . \mathbf{M}$ will operate in discrete stages; for each integer $m$, the $m$-th stage has $2^{m}$ phases.

At the initiation of the 0 -th phase of the $m$-th stage, the five counters of $\mathbf{M}$ will contain the following integers:

| Counter | Contents |
| :---: | :---: |
| $A$ | $2^{m-1}$ |
| $B$ | 0 |
| $C$ | 0 |
| $D$ | $2^{m-1}$ |
| $E$ | 0 |

[^4](i) Using counter $D, \mathbf{M}$ emits $2^{m} 0$ 's, one per time unit. Simultaneously, $\mathbf{M}$ loads counters $C$ and $E$ with $2^{m}$. Using finite state control, the roles of $C$ and $D$ are interchanged.
(ii) Using counters $A$ and $B$, by successive divisions by $2, \mathbf{M}$ emits the binary representation of the contents of $A$. Simultaneously, $\mathbf{M}$ loads $C$ with the original contents of $A$. At the end of this process, counters $A$ and $B$ are both 0 , and counter $C$ contains the integer that was in $A$ originally. The roles of $A$ and $C$ are interchanged.
(iii) Counter $A$ is incremented by +1 and counter $E$ by -1 .

The next phase ensues.
At the initiation of the $k$-th phase of the $m$-th stage ( $1 \leqslant k<2^{m}$ ), the five counters of $\mathbf{M}$ will contain the following integers:

| Counter | Contents |
| :---: | :---: |
| $A$ | $2^{m-1}+k$ |
| $B$ | 0 |
| $C$ | 0 |
| $D$ | $2^{m}$ |
| $E$ | $2^{m}-k$ |

(iv) Using counters $C$ and $D, \mathbf{M}$ emits a string of $2^{m} 0^{\prime}$ 's, one per time unit. The roles of $C$ and $D$ are then interchanged.

Steps (ii) and (iii) are now performed to complete this phase.
Note that the nullity of counter $E$ is used to signal a change of stage.
(b) $\propto \notin \mathbf{C}_{n}$

Note that, for every integer $m, \alpha$ contains $2^{m}$ distinct $m$-slices. By Lemma 1.1, $\alpha$ is not a real-time generable by any CM.
(c) $\alpha \in \mathbf{C}_{3 n}(5)$

During each of the $2^{m}$ phases of the $m$-th state, $\mathbf{M}$ emits $m+2^{m}$ symbols. The initial string of 0 's of length $2^{m}$ is emitted in real time. The remaining $m$ symbols are emitted in fewer than $2^{m+1}$ steps. ${ }^{8}$ Each phase thus takes fewer than $3 \cdot 2^{m}$ steps. To see this more precisely, assume that symbols $a_{0}, \ldots, a_{r}$ have been generated in at most $3 r$ steps, and that symbol $a_{r+1}$ is the first symbol to be emitted in the $k$-th

[^5]phase of the $m$-th stage. For each $i \in\left\{1, \ldots, 2^{m}\right\}$, symbol $a_{r+i}$ is emitted at step $3 r+i<3(r+i)$. Then, for each $j \in\{1, \ldots, m\}$, symbol $a_{r+2 m+j}$ is emitted before step
$$
3 r+2^{m}+\sum_{h=i}^{j} 2^{m-h+1}=3 r+2^{m}+2^{m+1}\left(\sum_{h=1}^{j} 2^{-h}\right)<3\left(r+2^{m}\right)<3\left(r+2^{m}+j\right) .
$$

Thus, $\mathbf{M}$ operates within time $T(n)=3 n$. Theorem 2.2, therefore, implies that $\alpha \in \mathbf{C}_{(\mathbf{1}+\boldsymbol{\epsilon})_{n}}$.

Thus, for counter machines, one obtains the same "partial" speedup for generation as for recognition [4]. For Turing machines, Theorem 2.1 indicates a full speedup for generation, whereas Rosenberg [10] showed there is only a partial speedup for recognition.

There is an important special case in which a full speedup is attainable. This result is immediate from Proposition 1.3(ii).

Proposition 2.1. Given a CM which emits an output at least every cesteps for some integer $c>0$, one can find an equivalent CM which operates in real time.

### 2.2. The Real-Time Hierarchy

In 1963, Rabin [9] proved that there were languages real-time recognizable by 2-tape TM's but not by 1 -tape TM's. For generation, it is not known if any number of tapes yields more real-time computing power than one tape.

Open Problem. Is there an $m>1$ such that $\mathbf{T}_{n}(m)-\mathbf{T}_{n}(1) \neq \emptyset$ ?
For counter machines, the current state of knowledge is more complete. A simple proof was presented in [4] to the effect that, for every $m$, there is a language which is real-time recognizable by an $(m+1)-$ CM but by no $m-$ CM. We now extend this result to CM generators (which can be viewed as recognizers for languages for a unary alphabet).

Theorem 2.4. For every integer $m>0, \mathbf{C}_{n}(m)$ is properly included in $\mathbf{C}_{n}(m+1)$.
Proof. We first remark that $\mathbf{C}_{n}(1)=\mathbf{C}_{n}(0)$ is the set of sequences generable by a finite state machine. Since each such sequence is ultimately periodic, it is an elementary excercise to find sequences in $\mathbf{C}_{n}(2)-\mathbf{C}_{n}(1)$.

The reader will easily verify, for example, that the sequence

$$
\alpha=a_{0}, a_{1}, \ldots
$$

such that $a_{i}=1$ iff $i$ is a perfect square suffices. The difficulty sets in when the $m$ of the theorem strictly exceeds 1 .

We now exhibit, for any given integer $m \geqslant 2$, a sequence $\alpha \in \mathbf{C}_{n}(m+1)-\mathbf{C}_{n}(m)$. Since weak inclusion of the sets is immediate, this demonstration, combined with the above remarks will complete the proof.

## (a) Generation of $\alpha^{9}$

Let $\mathbf{M}$ be a real-time ( $m+1$ ) - CM with counters labelled $A_{0}, \ldots, A_{m}$. M's computation is composed of alternating phases, an output phase, and an update phase. The initiation of the output phase is signalled by $A_{0}$ containing 0 and that of the update phase by $A_{1}$ containing 0 . (Both cannot occur simultaneously.)

Unless otherwise indicated, $\mathbf{M}$ emits a 0 at every step.
(i) Initialization: $\mathbf{M}$ adds +1 to $A_{1}$ and proceeds to the output phase.
(ii) Output phase: $\mathbf{M}$ increments $A_{\mathbf{0}}$ by 1 at each step, while simultaneously decrementing all other counters, repeating until $A_{1}$ contains 0 , at which point the update phase is entered. $\mathbf{M}$ emits a 1 at precisely those steps when at least one of the counters contained a 0 on the previous step.
(iii) Update phase: Let $j$ be the largest integer for which $A_{j}$ does not contain 0 . If $j=0$, set $j$ to $1 . \mathbf{M}$ first adds +1 to the current $A_{j}$. Then $\mathbf{M}$ simultaneously adds -1 to $A_{0}$ and +1 to $A_{1}, \ldots, A_{j}$, repeating until $A_{0}$ contains 0 . The output phase is then reentered.
(b) $\alpha \in \mathbf{C}_{n}(m+1)$

By definition, $\mathbf{M}$ generates $\alpha$ in real time.
(c) $\alpha \notin \mathbf{C}_{n}(m)$

In order to show that $\alpha \notin \mathbf{C}_{n}(m)$, we must analyze the structure of $\alpha$.
(i) Assume that at the beginning of an output phase, the counters of $\mathbf{M}$ contain integers $0, X_{1}, \ldots, X_{m}$, respectively, where $X_{1}>0$, and $X_{1} \geqslant X_{i}$ for $i \in\{2, \ldots, m\}$.

During this phase, $\mathbf{M}$ emits a slice of $\alpha$ of length $X_{1}+1$ which contains l's precisely in positions $1, X_{1}+1, \ldots, X_{m}+1 .{ }^{10}$ This phase ends with the counters of $\mathbf{M}$ containing the integers $X_{1}, 0, X_{2}-X_{1}, \ldots, X_{m}-X_{1}$, respectively.
(ii) Now assume that, at the beginning of an update phase, the counters of $M$ contain integers $X_{1}, 0, X_{2}-X_{1}, \ldots, X_{m}-X_{1}$, respectively, with some $X_{j}-X_{1} \neq 0$ while $X_{k}=X_{1}$ for $k>j$.

During this phase, $\mathbf{M}$ emits a slice of $\alpha$ of length $X_{1}+1$ which consists entirely of 0 's. The update phase ends with the counters of $\mathbf{M}$ containing the integers

[^6]$0, X_{1}, X_{2}, \ldots, X_{j-1}, X_{j}+1,0, \ldots, 0$, respectively. In the case that all $X_{i}=X_{1}$ for $i>1$, it ends with the counters of $\mathbf{M}$ containing $0, X_{1}+1,0, \ldots, 0$, respectively.

Note that, in effect the integers $X_{2}, \ldots, X_{m}$ are treated as the base $X_{1}+1$ representation of an integer, with $X_{2}$ being the high order digit. Thus, with each increment of this integer, carries propogate "to the left." The set of integers of length $m-1$ in base $X_{1}+1$ is exhausted when $X_{1}=X_{2}=\cdots=X_{m}$. At this point, $X_{1}$ is incremented, and $\mathbf{M}$ begins to enumerate the ( $m-1$ )-digit numbers in base $X_{1}+2$.

With this information about the structure of $\alpha$, we can resume our argument.
For each integer $k>1$, consider the set of $(k, m)$-words, i.e., length $k-1$ binary words containing at most ( $m-1$ ) l's. For every $k$, the number of $(k, m)$-words exceeds the binomial coefficient $\binom{k-1}{m-1}$. Moreover, for every $(k, m)$-word $w, \alpha$ contains a slice $0^{k} 1 w 10^{k} .{ }^{11}$

Now, let $r>2 m$ be any integer divisible by 4, and let $k$ be an integer in the range $r / 2 \leqslant k \leqslant 3 r / 4$. Each ( $k, m$ )-word $w$ appears in $r-k$ distinct $r$-slices of $\alpha$; namely,

$$
0^{i} 1 w 10^{r-k-1-i} \quad(i=0, \ldots, r-k-1) .
$$

Thus, the number of distinct $r$ slices of $\alpha$ must exceed

$$
\sum_{k \sim r ; 2}^{3 r / 4}(r \cdots k)\binom{k-1}{m-1}
$$

which, in turn, exceeds

$$
\sum_{k=r / 2}^{3 r / 4}(r-3 r / 4)\binom{r / 2-1}{m-1}
$$

which is no smaller than

$$
(r / 4)(r / 4)(r / 2-m+1)^{m-1} /(m-1)!
$$

This last expression is a polynomial in $r$ of degree $m+1$ with positive leading coefficient and, therefore, exceeds $c \cdot r^{m+1}$ for some fixed $c>0$.

Since, for sufficiently large $r, r^{m+1}>d \cdot r^{m}$ for any fixed $d$, the proof is now completed by appealing to Lemma 1.1.

Thus, the addition of a single counter increases the real-time computing power of CM's. Unfortunately, the present proof sheds no light on the corresponding problem for TM's. It would appear that new notions are needed to settle that open problem.

### 2.3. Operations on Sequences

In this section we consider the effect of operations on sequences on the classes $\mathrm{T}_{n}(\boldsymbol{m})$ and $\mathbf{C}_{n}(m)$. Another basic difference between TM's and CM's will emerge from this study.
${ }^{11}$ We let $0^{\boldsymbol{k}}$ denote a string of $k \mathbf{0}$ 's.

## (a) Compression Operations

Let $f$ be any function $f:\{0,1\}^{k} \rightarrow\{0,1\}$ for some $k$. A sequence $\beta=b_{0}, b_{1}, \ldots$ is an $f$ compression of a sequence $\alpha=a_{0}, a_{1}, \ldots$ if for each nonnegative integer $i$, $b_{i}=f\left(a_{i k}, a_{i:+1}, \ldots, a_{(i+1) k-1}\right)$.

Theorem 2.5. Let $\alpha$ be any sequence in $\mathbf{C}_{n}(m)$ [respectively, in $\left.\mathbf{T}_{n}(m)\right]$ for some $m$. Then every $f$ compression of $\alpha$ is also in $\mathbf{C}_{n}(m)$ [respectively, in $\left.\mathbf{T}_{n}(m)\right]$.

The proof is immediate since one can obviously construct a generator for any $f$ compression of $\alpha$ which would emit symbols at a constant rate.

## (b) Composite Sequences

As before, let $f$ be an arbitrary function mapping $\{0,1\}^{k /} \rightarrow\{0, I\}$. The sequence $\beta=b_{0}, b_{1}, \ldots$ is an $f$ composite of the $k$ sequences $\alpha_{i}=a_{i 0}, a_{i 1}, \ldots(1 \leqslant i \leqslant k)$ if, for every integer $j \geqslant 0, b_{j}=f\left(a_{1 j}, a_{2 j}, \ldots, a_{k j}\right)$.

The following result is obvious:
Proposition 2.2. If the $k$ sequences $\alpha_{1}, \ldots, \alpha_{k}$ are each in $\mathbf{C}_{n}(m)$ [respectively, in $\left.\mathrm{T}_{n}(m)\right]$ for some integer $m$, then any $f$ composite of the sequences is in $\mathbf{C}_{n}(k m)$ [respectively, in $\mathrm{T}_{n}(k m)$ ].

In this subsection we show that for TM's Proposition 2.2 can be significantly improved, while for CM's it cannot.

Theorem 2.6. If the $k$ sequences $\alpha_{1}, \ldots, \alpha_{k}$ are each in $\mathrm{T}_{n}(m)$ for some integer $m$, then any f-composite of the sequences is in $\mathrm{T}_{n}(m+4)$.

Proof. By Theorem 2.4, it will suffice to show that, for some integer $c>0$, the composite sequence is in $\mathrm{T}_{\mathrm{cn}}(m+1)$. This, then, is our goal.

For $i=1, \ldots, k$, let the sequence $\alpha_{i}$ be generated by the real-time $m$-TM $\mathbf{M}_{i}$. We construct an $(m+1)$-TM $\mathbf{M}$ which will generate the $f$-composite sequence $\beta$.
(a) Structure of $\mathbf{M}$
$\mathbf{M}$ will have $m+1$ tapes, denoted $T_{0}, \ldots, T_{m}$, each having $k$ tracks. ${ }^{12}$ Tape $T_{0}$ will be used to control the generation of $\beta$. For $i=1, \ldots, k$, the $i$-th channels of tapes $T_{1}, \ldots, T_{m}$ will be used to simulate $\mathbf{M}_{i}$.
(b) Operation of $\mathbf{M}$
$\mathbf{M}$ will operate in discrete stages, each comprising $k+1$ phases. Roughly speaking, during the $r$-th stage, $\mathbf{M}$ will generate $2^{r}$ symbols from each $\alpha_{i}$, hence, an equal number from $\beta$.

[^7]Let us assume that at the beginning of the $r$-th stage, $\mathbf{M}$ has the following information available:
(i) a segment of length $2^{r-1}$ is marked off on tape $T_{0}$;
(ii) for $i=1, \ldots, k$, the $i$-th channels of tapes $T_{1}, \ldots, T_{m}$ contain the same information as would the tapes of $\mathbf{M}_{i}$ after $2^{r}-1$ steps of its computation, and the squares on which $\mathbf{M}_{i}$ 's heads would reside are marked;
(iii) for $j=1, \ldots, m$, the head on tape $T_{j}$ resides on the square it initially occupied, which is also marked. The $r$-th stage now proceeds as follows:

Phase 0. Using the marked segment of length $2^{r-1}, \mathbf{M}$ uses tapes $T_{0}$ and $T_{1}$ to mark off a segment of length $2^{r}$ on tape $T_{0}$.

Phase i $(1 \leqslant i<k)$. $\mathbf{M}$ positions heads $1, \ldots, m$ on the squares marked on channel $i$ of each tape. $\mathbf{M}$ then simulates $\mathbf{M}_{i}$ for $2^{r}$ steps, recording the symbols $\mathbf{M}_{i}$ would emit on channel $i$ of tape $T_{0}$. The end of this simulation period is signalled by the head on $T_{0}$ reaching the end of the marked segment of length $2^{r}$. The head on $T_{0}$ then backspaces to the beginning of the segment, and the other heads backspace to their initial squares. Phase $i+1$ is now entered.

Phase k. Phase $k$ is almost identical to phase $\mathrm{k}-1$. However, instead of recording the symbols $\mathbf{M}_{k}$ would emit, $\mathbf{M}$ combines these symbols with the stored output from the preceding phases and emits the next $2^{r}$-slice of $\beta$.

The reader should be able to fill in the details about M's operation.

## (c) Timing Estimates

We estimate the time required for the $r$-th stage.
(i) Phase 1 clearly requires at most $2^{r}$ steps.
(ii) Since, by the end of the $(r-1)$-th stage, $2^{r}-1$ symbols of each $\alpha_{i}$ have been generated, at most $2^{r}-1$ steps are required to position the heads of $\mathbf{M}$ in each phase. Each $\mathbf{M}_{i}$ is then simulated for $2^{r}$ steps. Finally, the heads are backspaced at most $2^{r+1}$ squares. Thus, the $r$-th stage, during which a $2^{r}$-slice of $\beta$ is generated, requires at most $(4 k+1) \cdot 2^{r}$ steps. Therefore, $\mathbf{M}$ operates within time $T(n)=8 k \cdot n$. (The additional factor of 2 compensates for the fact that phases $0-(k-1)$ produce no output.)

Since $\beta$ is thus in $\mathbf{T}_{8 k n}(m+1)$, the theorem follows.
The reader will easily verify that Theorems 2.5 and 2.6 and Proposition 2.2 remain valid if the finite function $f$ is replaced by a generalized sequential machine mapping.

In contrast to Theorem 2.6, we find that the CM complexity classes are sensitive to even simple operations on sequences, such as OR-ing.
'Гheorem 2.7. For any integers $k, m>0$, there exist $m$ sequences $\alpha_{1}, \ldots, \alpha_{m}$, each in $\mathbf{C}_{k}(k+4)$, such that the sequence $\beta=\bigvee_{i=1}^{m} \alpha_{i}$ is not in $\mathbf{C}_{n}(k m) .{ }^{13}$

Proof. For $i-1, \ldots, m$, we describe a $(k+4)-\mathrm{CM} \mathbf{M}_{i}$ which generates $\alpha_{i}$ in real time. Let us focus on a specific $\mathbf{M}_{i}$.

For each integer $r$, let $p_{r}$ denote the $r$-th prime, and, for any integer $x$, let $\pi_{r}(x)$ be the exponent of $p_{r}$ in the prime factorization of $x$.

## (a) General Description of $\alpha_{i}$

In rough terms, $\mathbf{M}_{i}$ will generate a sequence of the form $z_{1} 1 w_{1} z_{2} l w_{2} \ldots$ where, for each integer $q, z_{q}$ is a sequence of $2 k q 0$ 's, and $w_{q}$ is a binary word of length $q+1$ having l's precisely in positions $\pi_{(i-1) k+1}(q), \ldots, \pi_{i k}(q)$.

## (b) Generation of $\alpha_{i}$

$\mathbf{M}_{i}$ will have $k+4$ counters: two process-control counters, $P_{1}$ and $P_{2}$, two computational counters, $C_{1}$ and $C_{2}$, and $k$ generating counters, $G_{1}, \ldots, G_{k}$.
$\mathbf{M}_{i}$ will operate in discrete stages, each stage comprising an update phase and an output phase. During the update phase of stage $q, \mathbf{M}_{i}$ will emit $z_{q} l$; during the output phase, it will emit $w_{q}$. This is accomplished as follows.

At the initiation of stage $q(q=1,2, \ldots)$ counters $P_{1}$ and $C_{1}$ will each contain $q$, and all other counters will be empty.
(i) Update Phase. Throughout the update phase, $\mathbf{M}_{i}$ emits a 0 at each step. At every (2k)-th step of the update phase, $\mathbf{M}_{i}$ adds -1 to $P_{1}$ and +1 to $P_{2}$. The phase terminates when $P_{1}$ is empty; then the output phase begins, and $\mathbf{M}_{i}$ emits a 1 .

While $\mathbf{M}_{i}$ is counting in its control counters, it stores the integer $\pi_{j}(q)$ in counter $G_{j}$ for $j(i \cdots 1) k+1, \ldots, i k$. This is accomplished by successive divisions of $q$ by $p_{(i-1) k, 1}, \ldots, p_{i k}$, using counters $C_{1}$ and $C_{2}$. Since computing $\pi_{j}(q)$ requires fewer than $p_{j} q /\left(p_{j}-1\right) \approx 2 q$ steps, all this computing can be done in fewer than $2 k q$ steps; hence, it can be completed during the update phase.
(ii) Output Phase. When the output phase begins, counter $P_{1}$ is empty, and $P_{2}$ contains $q$; one of $C_{1}$ and $C_{2}$ is empty (say $C_{1}$; by a change of name this can be assumed), and the other contains an integer less than $q$; and each $G_{j}$ contains the intager $\pi,(q)$.

At each step of the output phase: $\mathbf{M}_{i}$ adds +1 to $P_{1}$ and $C_{1}$ and adds -1 to $P_{2}$; it adds -1 to $C_{2}$ until $C_{2}$ is empty; it adds -1 to each $G_{j}$ until that $G_{j}$ is empty.

At each step an output is emitted. This output is a 0 unless some $G_{j}$ had become empty at the previous step, in which case all is emitted. When $P_{2}$ is empty, $\mathbf{M}_{i}$ adds +1 to $P_{1}$ and to $C_{1}$, emits a 0 , and enters stage $q+1$.
${ }^{13} \beta$ is the elementwise OR of the $\alpha_{t}$.
(c) The Sequence $\beta$

For any integer $s$, and for any $s$ tuple of integers $\left\langle y_{1}, \ldots, y_{s}\right\rangle$, there is obviously an integer $x$ such that, for $i=1, \ldots, s, \pi_{i}(x)=y_{i}$. Therefore, the sequence $\beta$ is of the form $z_{1}\left|w_{1} z_{2}\right| w_{2}, \ldots$ where each $z_{q}$ is a sequence of $2 k q 0$ 's and each $w_{q}$ is a binary word of length $q+1$ having I's precisely in positions $\pi_{1}(q), \ldots, \pi_{k m}(q)$.

The sequence $\beta$ must, then, contain every slice of the form $0^{q} 1 w 10^{q}$ where $w$ is a binary word of length not exceeding $q$ containing at most $k m-1$ I's. The Theorem now follows by repeating the argument of Theorem 2.4.

By clever programming the constant 4 of the Theorem can undoubtedly be reduced, but it can probably not be set to 0 .

## 3. Real-Time Countable Functions

The notion of a real-time countable function was introduced by Yamada [12] as a convenient way to describe real-time generable sequences. We shall extend the work of Yamada both by generalizing a number of his results to CM generators as well as TM's, and by developing a general technique for finding and identifying real-time countable functions.

One can associate with any sequence $\alpha=a_{0}, a_{1}, \ldots$ containing infinitely many l's a strictly increasing recursive function $f$ as follows:

$$
\begin{gathered}
f(0)=\mu y\left[a_{v}=1\right] \\
f(x+1)=\mu y\left[y>f(x) \& a_{v}=1\right] .
\end{gathered}
$$

Thus, $\alpha$ is a characteristic sequence for the range of $f$. If $\alpha$ is real-time generable, then $f$ is termed real-time countable, and the real-time generator for $\alpha$ is called a counter for $f$.

It is essential to note that a counter for $f$ does not compute $f$ in the conventional sense of the word. We shall say that an $n$-CM computes a function $f$ under the following conditions. If the $n-\mathrm{CM}$ is started with an integer $x$ in counter 1 and all other counters empty, it will enter a designated state with $f(x)$ in counter $n$ and all other counters empty. The $n$-CM is said to compute $f$ within (respectively, in) time $T$ if, for any $x$, at most (respectively, exactly) $T(x)$ steps elapse between the time the $n$-CM is started with $x$ in counter 1 to the time it enters the designated state. Similar conventions apply when the counter for $f$ is a TM.

Thus, real-time countable functions should not be confused with functions which can be computed within slowly growing time functions. In fact, any recursive function can be majorized by some real-time countable function, so there are arbitrarily large real-time countable functions which must take arbitrarily long to compute. [The time required to compute $f(x)$ cannot, by definition, be less than $f(x)$.] Rather,
real-time countability is related to the property that a function can be computed in time proportional to its own values. We now formalize this property.

A function $f(x) \geqslant x$ is self-computable if there is an integer $k>0$ such that, for all $x, f(x)$ is computable within time $k \cdot f(x)$.

The definition of real-time countability readily yields

Proposition 3.1. Every real-time CM (TM) countable function is self-computable on a CM (respectively, TM).

The remainder of this section is devoted to proving a partial converse to Proposition 3.1. This converse will provide a powerful tool for establishing the real-time countability of functions. It can be used to simplify several known proofs and settle certain open questions.

We now lay the groundwork for a partial converse to Proposition 3.1.
We say a function $f(x) \geqslant x$ is strongly self-computable if there is an integer $k>0$ such that, for all $x, f(x)$ is computable in time $k \cdot f(x)$.

The following lemma is of central importance in the sequel.

Lemma 3.1. A function $f(x) \geqslant x$ is self-computable on $a \mathrm{CM}$ (TM) if, and only if, it is strongly self-computable on a CM (respectively, TM).

Proof. Sufficiency being self-evident, we establish necessity. Let $\mathbf{M}$ be an $n$-CM which computes $f$ within time $k \cdot f(x)$ for some integer $k>0$. Consider the ( $n+2$ )-CM $\mathbf{N}$ which operates as follows. A computation by $\mathbf{N}$ begins with an integer $x$ in counter 1 and all other counters empty.
(a) $\mathbf{N}$ simulates $\mathbf{M}$ on its first $n$ counters, adding 1 to counter $n+1$ for each step of the computation. After some number $t \leqslant k \cdot f(x)$ steps, when $\mathbf{M}$ would halt, $\mathbf{N}$ has the following configuration:
(i) counters $1, \ldots,(n-1)$ are empty;
(ii) counter $n$ contains $f(x)$;
(iii) counter $n+1$ contains $t \leqslant k \cdot f(x)$;
(iv) counter $n+2$ is empty.
(b) N now begins decrementing counter $n$, while handling counters $n+1, n+2$ in the following way. For each decrement from counter $n$, a 1 is added to counter $n+2$. Similarly, for each such decrement, $k$ is subtracted from counter $n+1$ until that counter is emptied. Once counter $n+1$ has been depleted, subtractions from that counter are replaced by additions to it. (Thus, we are acting as though counter $n+1$ had contained $-t$ and we had consistently added $k$ to it.) At the end of this process, a total of $t+k \cdot f(x)$ steps have elapsed, and $\mathbf{N}$ has the following configuration:
(i) counters $1, \ldots, n$ are empty;
(ii) counter $n+1$ contains $k \cdot f(x)-t$;
(iii) counter $n+2$ contains $f(x)$.
(c) $\mathbf{N}$ finally empties counter $\boldsymbol{n}+1$ and halts in a designated state. Thus, after precisely $k \cdot f(x)+t+k \cdot f(x)-t=2 k \cdot f(x)$ steps, $\mathbf{N}$ has arrived at the following configuration:
(i) counters $1, \ldots, n+1$ are empty;
(ii) counter $n+2$ contains $f(x)$.
$f$ is thus shown to be strongly self-computable. Since a TM tape can readily act as a counter with no time loss, if $\mathbf{M}$ is an $n$-TM, N can be taken to be an ( $n+2$ )-TM. The lemma thus holds for TM's as well.

The following lemma is immediate by Proposition 1.3(ii) and the proof techniques of Theorem 2.2.

Lemma 3.2. Let $\mathbf{M}$ be an $m$-CM which computes a function $f$ in time $k \cdot f$ for some integer $k>0$. For any integer $c>0$, one can effectively obtain from $\mathbf{M}$ an $m-C M N$ which operates as follows:

For any integer $x \geqslant 0$, when $\mathbf{N}$ is started with $[x / c]$ in its first counter, all other counters empty, and $x-c[x / c]$ in finite state memory, $\mathbf{N}$ will enter a designated state in $\lceil k \cdot f(x) / c\rceil$ steps with $[f(x) / c]$ in its last counter, all other counters empty, and $f(x)-c[f(x) / c]$ in finite state memory. ${ }^{14}$

We refer to the function which $\mathbf{N}$ computes as $f^{(c)}$.
Given a function $f$, let $\Delta f(x)=f(x+1)-f(x)$. If $f$ is increasing, $\Delta f$ obviously takes on only positive values.

Theorem 3.1. Let $f$ be a strictly increasing function. If $\Delta f$ is self-computable on a CM (TM) then $f$ is real-time countable on a CM (respectively, TM).

Proof. The TM version follows directly from the CM construction given. Let $\mathbf{M}$ be an $m$-CM which computes $\Delta f$ in time $k \cdot \Delta f$ for some integer $k>0$. By Lemma 3.1, such an $M$ exists; moreover, by Lemma 3.2 we can obtain, from $\mathbf{M}$, another $m$-CM N which computes the function $\Delta f^{(2 k)}$. We now construct an $(m+2)$-CM $\mathbf{P}$ which is a real-time counter for $f$.
(a) Structure of $\mathbf{P}$
$\mathbf{P}$ will have two counters $P_{1}$ and $P_{2}$ to control the process of counting $f$, and it will have $m$ counters $C_{1}, \ldots, C_{m}$ with which to simulate $\mathbf{N}$. It will further have two bounded registers in finite state memory, which can hold any integer not exceeding $2 k$.

[^8]
## (b) Operation of $\mathbf{P}$

$\mathbf{P}$ will operate in discrete stages. For each integer $q$, at the beginning of stage $q$, $\mathbf{P}$ will have the integer $[q / 2 k]$ in counters $P_{1}$ and $C_{1}, 0$ in all other counters, and $q-2 k[q / 2 k]$ in finite memory.

While emitting 0 's at every step, $\mathbf{P}$ simulates $\mathbf{N}$ on counters $C_{1}, \ldots, C_{m}$ until $\mathbf{N}$ would complete its computation. At this point, $P_{1}$ and $P_{2}$ are unchanged, $C_{1}, \ldots, C_{m-1}$ are empty, $C_{m}$ contains $[\Delta f(q) / 2 k]$, and $[k \cdot \Delta f(q) / 2 k]=\lceil\Delta f(q) / 2\rceil$ steps have elapsed.
$\mathbf{P}$ now empties $C_{m}$, subtracting 1 every $k$ time units, and simultaneously transfers the contents of $P_{1}$ to both $P_{2}$ and $C_{1}$. The reader will readily see that, when $C_{m}$ is finally emptied, $\mathbf{P}$ can emit a 1 after an additional delay of fewer than $k$ steps and make the necessary adjustments for the next stage of the process.
We leave it to the reader to fill in the minor details of $P$ 's operation.
Thus, the $q$-th stage of $\mathbf{P}$ 's computation takes precisely $\Delta f(q)$ steps. Therefore, inductively, if $\mathbf{P}$ emitted a 1 at time $f(q)$, it would again emit a 1 at time $f(q)+\Delta f(q)=f(q+1)$. That is to say, $\mathbf{P}$ is a real-time counter for $f$.

Theorem 3.1 and Proposition 3.1 immediately yield

Corollary. Let $f$ be an increasing function. If $\Delta f$ is real-time countable, then $f$ is also.
A second corollary of Theorem 3.1 and Lemma 3.1 has wide application.
Corollary. Let $f$ be an increasing self-computable function such that, for some real number $a \geqslant 2$ and for all integers $x, f(x+1) \geqslant a \cdot f(x)$. Then $f$ is real-time countable.

Proof. By hypothesis, $f(x)$ is computable within time $k \cdot f(x)$ for some integer $k>0$. Thus, the obvious method of computing $\Delta f(x)$, namely computing $f(x)$, then $f(x+1)$, then computing their difference, requires at most $3 k \cdot f(x+1)$ steps.

Now, since $f(x+1) \geqslant a \cdot f(x)$, it is clear that

$$
\Delta f(x)=f(x+1)-f(x) \geqslant f(x+1)-f(x+1) / a=((a-1) / a) f(x+1)
$$

whence, $f(x+1) \leqslant(a /(a-1)) \Delta f(x)$.
Therefore, we have (i) $\Delta f(x)$ is computable within time $\lceil(3 k a /(a-1))\rceil \Delta f(x)$, and (ii) $\Delta f(x) \geqslant(a-1) f(x) \geqslant f(x) \geqslant x$. Thus, $\Delta f$ is self-computable, and the result follows by Lemma 3.1 and Theorem 3.1.
In order to gauge the utility of the previous results, we note that many familiar arithmetic functions are self-computable (and, in fact, real-time countable on a CM). For example, all polynomials with nonnegative integer coefficients are real-time countable on a CM, as are $f(x)=c^{x}$ for any positive integer $c$ and $g(x)=x!$. With such common functions as starting points, one can infer the self-computability and/or real-time countability of other functions from the folloming closure properties:

Proposition 3.2. If $f$ and $g$ are self-computable on a CM (TM), then so are the following functions:
(a) $h_{1}(x)=f(x)+g(x) ;$
(b) $h_{2}(x):=f(x) \cdot g(x)$;
(c) $h_{3}(x):=f(x)^{g(x)}$;
(d) $h_{4}(x):=f(x)!$;
(e) $h_{5}(x):=f(g(x))$;
(f) $\quad h_{B}(x):=\sum_{i=0}^{x} f(i) ;$
(g) $h_{7}(x)=\prod_{i=0}^{x} f(i)$.

Proof. It is a straightforward exercise to show that each of the noted functions is computable within time proportional to its values. For example, to compute $f(g(x))$ :
(i) Compute $g(x)$ in $k_{1} g(x)$ steps.
(ii) Compute $f(g(x))$ in $k_{2} f(g(x))$ steps.

This computation thus takes $k_{2} f(g(x))+k_{1} g(x) \leqslant\left(k_{1} \div k_{2}\right) f(g(x))$ steps, whence $h_{5}$ is self-computable.

The other proofs are similar and are left to the reader.
The techniques developed in this section afford powerful tools for establishing the real-time countability of functions. In fact, they suffice to show that every function explicitly shown by Yamada to be real-time countable on a TM is, in fact, real-time countable on a CM. However, not all functions which are real-time countable in Yamada's sense (i.e., on a TM) are real-time countable on a CM as the corollaries to Lemma 1.1 and 1.2 readily show.

As a final remark, we note that Lemma 3.2 has a rather strong implication for CM speedup. The lemma can be expanded on to show that, for sequences $\alpha$ in which 1's are sufficiently sparse, ${ }^{15}$ one can obtain a full speedup for CM's. This supplements the special case of Proposition 2.1.

## 4. Simulation of One Machine by Another

### 4.1. Simulation of CM's by TM's

The following result was proved by the authors in [4].

[^9]'Тheorem 4.1. Given any $k$-CM recognizer which operates within time $T(n)$ and space $S(n)$, one can effectively find, for any $\in>0$, an equivalent $1-\mathrm{TM}$ recognizer which operates within time $(1+\epsilon) T(n)$ and space $\log S(n)$.

The "full" speedup for TM generators (Theorem 2.1) affords us the following strengthened version of Theorem 4.1.

Theorem 4.2. For any integer $k$ and real-time countable function $T, \mathbf{C}_{T(n)}(k) \subseteq \mathbf{T}_{T(n)}(4)$.
In other words, every $k$-CM can effectively be replaced by a time-equivalent 4-TM. It is an interesting open problem whether or not the number of tapes can be reduced, e.g., to one.

### 4.2. Simulation of TM's by CM's

We begin by presenting an upper bound on the time required by an $m$ - CM to simulate a $k$-TM.

Theorem 4.3. Let $\mathbf{M}$ be a $k$-TM with w working symbols which operates in time $T$ and space $S$. There are constants $u>1$ and $v$ such that for any integer $r>0$, one can effectively find $a(2 r+1) k-\mathrm{CM} \mathbf{N}$, equivalent to $\mathbf{M}$ which operates within time

$$
\begin{aligned}
T^{\prime}(n) & \leqslant v \cdot \sum_{i=1}^{n}\left[T(i)-T\left(i^{i} \cdot 1\right)\right] \cdot w^{[S(i) / r]} \\
& <v \cdot T(n) w^{[S(n) / r]} \\
& <u^{[T(n) / r]}
\end{aligned}
$$

Proof. We prove the result for the case $k=1$, the extension to arbitrary $k$ being immediate.
(a) Structure of $\mathbf{N}$
$\mathbf{N}$ will have $2 r+1$ counters named $R_{0}, \ldots, R_{r-1}$ (these will simulate the right side of M's tape), $L_{0}, \ldots, L_{r-1}$ (which will simulate the left side of M's tape), and $A$ (an auxiliary counter). In addition, the finite memory of $\mathbf{N}$ will have a bounded register, capable of holding any nonnegative integer less than $w$.

## (b) Correspondence Between Configurations

Let $\mathbf{M}$ have working alphabet $\left\{S_{0}, \ldots, S_{w-1}\right\}, S_{0}$ being the blank symbol. Associate with each symbol $S_{i}(0 \leqslant i<w)$ the integer label $\left|S_{i}\right|=i$.

Assume that, at some state in its computation, the nonblank portion of M's tape is of the form

$$
a_{p} a_{p-1} \cdots a_{0} c b_{0} b_{1} \cdots b_{q}
$$

with the read-write head residing on $c$. At the corresponding point in the simulation, counter $R_{d}(0 \leqslant d<r)$ of $\mathbf{M}$ will contain the integer

$$
x_{d}=\sum_{i=0}^{\lceil q / r 1}\left|b_{i r+d}\right| w^{i},
$$

counter $L_{e}(0 \leqslant e<r)$ of $\mathbf{M}$ will contain the integer

$$
y_{e}=\sum_{i=0}^{\lceil p / r]}\left|a_{i r+e}\right| w^{i},
$$

counter $A$ will contain 0 , and the bounded register will contain $|c|$.

## (c) Simulation of $\mathbf{M}$ by $\mathbf{N}$

Assume now that $\mathbf{M}$ overwrites symbol $c$ by $c^{\prime}$ and shifts its head to the left to arrive at the configuration

$$
a_{p} a_{p-1} \cdots a_{0} c^{\prime} b_{0} b_{1} \cdots b_{a}
$$

with the read-write head residing on $a_{0} . \mathbf{N}$ responds with the following actions:
(i) Using finite state control, N renames each counter $R_{d}$ as $R_{d+1}(0 \leqslant d<r-1)$. It then renames the original counter $R_{r-1}$ as $R_{0}$ and places the integer

$$
x=x_{r-1} \cdot w+c^{\prime}
$$

into the new $R_{0}$ (using $A$ as an auxiliary counter).
(ii) Similarly, $\mathbf{N}$ renames counter $L_{e}$ as $L_{e-1}(1 \leqslant e \leqslant r-1)$. It then renames the original counter $L_{0}$ as $L_{r-1}$ and places the integer

$$
y=\left[y_{r-1} / w\right]
$$

into the new $L_{r-1}$ (again using $A$ ).
(iii) Finally $\mathbf{N}$ replaces the contents of the bounded register by

$$
y_{r-1}-w\left[y_{r-1} / w\right]=\left|a_{0}\right| .
$$

## (d) Timing Estimates

One easily verifies that $\mathbf{N}$ 's simulation of a step of $\mathbf{M}$ above requires at most $2 w\left(w^{[(p+1) / r]}+w^{\Gamma(q+1) / r]}\right)$ steps.

Now, if $\mathbf{M}$ operates in space $S(n)$, then, for the $T(i)-T(i-1)$ steps between the $(i-1)$-th and $i$-th outputs, we must have $p+q+3 \leqslant S(i)$. The simulation by $\mathbf{N}$ of a step by $\mathbf{M}$ in this time interval requires, therefore, at most $4 w^{[S(i) / r+2]}$ steps. The result now follows immediately.

A commonly occurring special case yields an interesting corollary.

Corollary. Let $\mathbf{M}$ be a $k$-TM with w working symbols which operates in time $T(n)$ and space $S(n)=\log T(n)$. One can find, for any integer $q>0$, a $(2 q+1) k$-CM equivalent to $\mathbf{M}$ which operates within time $T^{\prime}(n)=v \cdot\left\lceil T(n)^{1+(\log w) / q}\right\rceil$ for some $v>0$.

This corollary and the sequel lend import to the following:
Open Problem. Given a TM which operates in time $T(n)$, does there exist a time-equivalent TM which operates in space $S(n)=\log T(n)$ ?

We now establish a lemma which yields a lower bound on the time required by a CM to simulate a TM. If the above open probiem is settied in the affirmative, then the lower bound obtained is not bad; otherwise, it is quite weak.

Lemma 4.1. Let $\alpha$ be an infinite binary sequence, and let $f$ and $g$ be functions with the following property: For infinitely many $n>0$, the initial $f(n)$ slice of $\alpha$ contains at least $g(n)$ distinct $n$ slices. If $\alpha$ is generable within time $T$ by an $m-\mathrm{CM}$, then

$$
\sup _{n \rightarrow \infty} \frac{n T(f(n))}{g(n)^{1+1 / m}}>0 .
$$

Proof. Let $\mathbf{M}$ be an $m$ - CM with $q$ states which generates $\alpha$ within time T. Let $w_{i}[i=1, \ldots, g(n)]$ be distinct $n$ slices in the initial $f(n)$ slice of $\alpha$, and let $t_{i}[i=1, \ldots, g(n)]$ be the time at which the first digit of $w_{i}$ appears in M's generation of $\alpha$. Since the $w_{i}$ are all distinct, the configurations of $\mathbf{M}$ at times $t_{1}, \ldots, t_{g(n)}$ must also be distinct. There must, therefore, be a least integer $d>0$ such that more than half of these $g(n)$ distinct configurations are mutually $d$ inequivalent.

Since the number of $d$ inequivalent configurations of $\mathbf{M}$ cannot exceed $q \cdot d^{m}$, we must have

$$
q \cdot d^{m}>g(n) / 2
$$

whence

$$
d>k \cdot g(n)^{1 / m},
$$

where $k=(2 q)^{-1 / m}$.
For $i=1, \ldots, g(n)$, let $u_{i}$ be the time at which $\mathbf{M}$ emits the last digit of $w_{i}$, and refer to $u_{i}-t_{i}$ as the time required for $w_{i}$. Note that, since the $w_{i}$ are all distinct, if $\mathbf{M}$ is in $e$-equivalent configurations (for some $e>0$ ) at times $t_{i}$ and $t_{j} \neq t_{i}$, then the time required for each of $w_{i}$ and $w_{j}$ must exceed $e$.

Now, by definition of $d$, at least half of the configurations of $\mathbf{M}$ at times $t_{1}, \ldots, t_{g(n)}$ are ( $d-1$ )-equivalent to some other of these configurations. Thus, no fewer than half of the $n$ slices $w_{1}, \ldots, w_{g(n)}$ each requires at least time $d$. Of these $g(n) / 2$ slices requiring no less than time $d$, at least $g(n) / 2 n$ do not overlap in $\alpha$. ( $w_{i}$ and $w_{j}$ do not overlap if either $u_{i}<t_{j}$ or $u_{j}<t_{i}$.) These nonoverlapping slices, therefore, account
for at least $d \cdot g(n) / 2 n$ steps during M's emitting the initial $f(n)$ slice of $\alpha$. We must then have

$$
T(f(n)) \geqslant d \cdot g(n) / 2 n>(k / n) \cdot g(n)^{1+1 / m}
$$

where $k=\left(2^{m+1} q\right)^{-1 / m}$, and the lemma follows.
Q.E.D.

Theorem 4.4. The sequence of binary representations of integers is real-time generable by a 1-TM but requires time $T$ on any $m$-CM where

$$
\sup _{n \rightarrow \infty} \frac{\log n T(n)}{n^{1+1 / m}}>0
$$

The sequence referred to obviously satisfies the lemma with $f(n)=2^{n}$ and $g(n)=2^{n} / 3$.

As a corollary to Theorem 4.4, we obtain the following result which was already obvious from the tools of Section 1.3.

Corollary 4.4. $\quad \mathrm{T}_{n}(\mathrm{l})-\mathbf{C}_{n} \neq \varnothing$.

## 4.3. $(m+1)$-CM's and $m$-CM's

In this final section we investigate the time required for an $m$-CM to simulate a CM with more counters. Corresponding problems for TM's are discussed by Hartmanis and Stearns [5] and by Hennie and Stearns [6].

By our previous remark that $0-C M$ 's and $1-C M$ 's are both equivalent to finite state machines (as sequence generators), we need consider only machines with $m \geqslant 2$.

The following theorem gives a coarse upper bound, which regrettably does not depend upon the number of counters of the machine $\mathbf{M}$ being simulated.

Theorem 4.5. Given a CM M which operates within time $T(n)$, one can effectively find an $r$-CM $(r \geqslant 2)$ which operates within time $T^{\prime}(n) \leqslant c T(n)^{1+d /(r-1)}$ for some positive constants cand $d$.

Proof. (a) Since $\mathbf{M}$ operates within time $T(n)$, it also operates within space $T(n)$.
(b) Using Theorem 4.1, we can find a $1-\mathrm{TM} \mathbf{N}$, equivalent to $\mathbf{M}$, which operates within time $T(n)$ and space $\log T(n)$.
(c) Using the corollary to Theorem 4.3 , we can find, for any $q>0$, a $(2 q+1)-\mathrm{CM}$, equivalent to N , which operates within time $c[T(n)]^{1+(\log w) / q}$ where $w$ is the number of symbols in the alphabet of $\mathbf{N}$ and $c$ is a constant. Choosing $r=2 q+1$, and $d=2 \cdot \log w$ we obtain the stated result.

Other simulation results were reported in [4].
Using Lemma 4.1, we can find a lower bound on the time required by an $r$-CM to simulate an $m$-CM.

Theorem 4.6. The sequence $\alpha_{n}$ which was shown to be in $\mathbf{C}_{n}(m+1)-\mathbf{C}_{n}(m)$ requires time $T$ on any $r$ - $\mathrm{CM}(r \leqslant m)$, where

$$
\sup _{n \rightarrow \infty}\left\{T(n) / n^{1+\mid m-r+1 /(n ;-1) r]}\right\}>0 .
$$

Proof. Note that the sequence in question has $f(n)=n^{m+1}$ and $g(n)=a \cdot f(n)$ for some $a>0$. Thus

$$
T(f(n))>k \cdot[f(n)]^{-1 /(m+1)} \cdot[a f(n)]^{1+1 / r}
$$

The result now follows immediately.

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[^0]:    * A portion of this paper was presented at the Eighth Annual Symposium on Switching and Automata Theory, Austin, Texas, October 18-20, 1967. See Reference [3].
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[^1]:    ${ }^{1}$ For any set $S$, we denote by $S^{n}$ the set of $n$ tuples of elements of $S$.

[^2]:    ${ }^{2} \mu$ is the minimalization operator of recursive function theory. For any propositional $P(x)$, $\mu y[P(y)]$ is the least integer $n$ such that $P(n)$, if any exists; it is undefined otherwise.
    ${ }^{3}$ Note that the sequence $\alpha$ can be null, finite, or infinite. We assume in the sequel that all sequences generated are infinite.
    ${ }^{4}$ For any real number $x>0,[x]$ denotes the integer part of $x$.

[^3]:    ${ }^{5}$ All logarithms in this paper are to the base 2.
    ${ }^{6}$ The result does not follow from [5] alone, since our generators can, in a single step, emit at most one binary digit and not a sequence thereof.

[^4]:    ${ }^{7}$ Private communication.

[^5]:    ${ }^{8}$ The first division takes fewer than $2^{m}$ steps, the second fewer than $2^{m-1}$, etc.

[^6]:    ${ }^{9}$ For this proof we revert to the original definition of CM which permits negative integers in the counters.
    ${ }^{10}$ Of course some $X_{i}$ may equal some $X_{j}$, so some of these $m+1$ positions may be the same.

[^7]:    ${ }^{12}$ The reader who wishes to avoid the anthropomorphic notion of tracks can read this as saying that the symbols written on $T_{0}$ are from an alphabet which is a cross product of $k$ smaller alphabets.

[^8]:    ${ }^{14}$ For any real number $x,\lceil x\rceil$ denotes the least integer $n>x$.

[^9]:    ${ }^{15}$ To make the notion, "sufficiently sparse," more precise, let $d_{\alpha}(i, j)$ be the distance between the ith and $j$ th 1 's in the sequence $\alpha$. We then say that $\alpha$ is "sufficiently sparse" if, for some $\epsilon>0$ and for all $n, d_{a}(n, n+1)>(1+\epsilon) d_{\alpha}(n-1, n)$.

