

TO: Distribution  
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 SUBJECT: Installation of MSS 32.1

## I. SYSTEM CHANGE LOG

## MCR MODIFICATIONS FOR MSS 32.1

- 2579 Install a concurrent scheduler.  
 - many functions no longer wire, mask, and lock the APT;  
 - some critical loops are c 50% faster;  
 - a new process state (ptlocking) has been invented;  
 - load and clear (rather than read alter rewrite) instructions are used to manipulate all scheduler locks.
- 2569 Add a unique ring 0 wakeup entry to pxss. This should eliminate the most common cause of itt overflow system crashes: excessive itt messages generated by hardcore ttydim.

## II. SEGMENT CHANGE LOG

SEGMENT NAME	BOUND SEGMENT
act_proc.pll	R bound_process_creation
core_queue_man.alm	R bound_page_control
device_control.alm	R bound_page_control
emergency_shutdown.alm	R -
hardcore.header	R -
hphcs.alm	R -
page.alm	R bound_page_control
page_fault.alm	R bound_page_control
pxss.alm	R -
set_alarm_timer.pll	R bound_tc_wired
tc.pll	R bound_hc_tuning
tc_data	R -
tc_init.pll	R bound_init_1
wired_plm.pll	R bound_tc_wired

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II. SEGMENT CHANGE LOG (continued)

BIND SEGMENTS

bound\_page\_control.bind R

INCLUDE SEGMENTS

apte.incl.alm R  
apte.incl.pll R  
state\_equs.incl.alm R  
tc\_meters.incl.alm R  
tc\_m.incl.pll R