

TO: Distribution

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SUBJECT: Installation of MSS 23.9 & MSS 23.9a

The hardcore system MSS 23.9 was installed at 0900 on Thursday, May 2, 1974. This system includes: implementation of a new core-freeing mechanism in initialization to make use of currently unused core; a change to allow Terminet 1200's to work with the Vadic modems at 1200 baud; a change to speed up the metering gates for five hcs_ entries by using the fast gate strategy; and, numerous bug fixes. The system libraries were updated at 1515 on Thursday, May 2, 1974.

The hardcore system MSS 23.9a was installed at 1115 on Friday, May 3, 1974. This system includes a bug fix for tty_index introduced in MSS 23.9. The system libraries were updated at 1152 on Monday, May 6, 1974.

I. System Change Log

Chng MCR

Change

- # 1290 393 change to not reference tty_buf\$ in salvager case.
- # 1291 426 implement new core-freeing mechanism in initialization which fixes bugs that caused 13 pages of core to currently go unused.
- # 1292 333,410
fixes tty DIM bugs and add changes to allow Terminet 1200's to work at 1200 baud with the Vadic modems.
- # 1293 384,386
removes occasional fatal process error due to IOX IPS masking bug, and corrects error code returned by the "seek_key" I/O operation of the "discard_" DIM.
- # 1294 387 retain entry point "lox_\$control".

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I. System Change Log (continued)

Chng MGRChange

- # 1295 347 fixes bug in parity_fault which caused instruction and data fetch retries to be reported incorrectly.
- # 1296 419 corrects bug in salv_truncate to correctly call pc\$truncate.
- # 1297 383,385
speed up metering gates on five entries of hcs_ by using the fast gate strategy, and, tighten IPS masking interface.
- # 1298 - fix bug in tty_index introduced in MSS 23.9.

II. Segment Change Log

<u>Segment Name</u>	<u>Bound Segment</u>	<u>Change #</u>
bootstrap1	-	R 1291
collect_free_core	bound_temp_1	A 1291
debug_check	bound_error_wired	R 1290
discard_attach	bound_lox_	R 1293
find_locb	bound_lox_	R 1293
hcs_	-	R 1297
init_sst	bound_temp_1	R 1297
ios_	bound_command_loop_	R 1297
iox_	bound_lox_	R 1294
ips_	bound_file_system	R 1294
ntape_attach	bound_lox_	R 1294
parity_fault	bound_error_active	R 1295
privileged_mode_ut	bound_priv_1	R 1295
salv_truncate	bound_salvager	R 1296
syn_attach	bound_lox_	R 1296
tty_attach	bound_lox_	R 1296
tty_ctl	-	R 1292
tty_index	bound_tty_active	R 1298
tty_write	bound_tty_active	R 1298
update_sst_pl1	bound_temp_1	R 1298
<u>Include Files</u>		
ards.incl.ioc		R 1292
convtab.incl.pl1		R 1292
equ.incl.ioc		R 1292
fab.incl.ioc		R 1292
tty.incl.pl1		R 1292

II. Segment Change Log (continued)

<u>Segment Name</u>	<u>Bound Segment</u>	<u>Change #</u>
<u>Bindfiles</u>		
bound_file_system.bind		R 1297
bound_lox_.bind		R 1294
bound_temp_1.bind		R 1291

III. Bound Segment Change Log

<u>Bound Segment</u>	<u>Change</u>
bound_error_wired	replaced: debug_check
bound_temp_1	added: collect_free_core replaced: init_sst update_sst_pl1
bound_priv_1	replaced: privileged_mode_ut
bound_tty_active	replaced: tty_write tty_index
bound_command_loop_	replaced: los_
bound_error_active	replaced: parity_fault
bound_salvager	replaced: salv_truncate
bound_lox_	replaced: lox_ tty_attach find_locb syn_attach ntape_attach discard_attach
bound_file_system	replaced: ips_

IV. Header Change Log

The header was changed to move the entry temp_copyseg_1 from collection 1 to collection 2.