

TO: Distribution  
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SUBJECT: Installation of MSS 29.2, and 29.2a

### I. SYSTEM CHANGE LOG

#### MCR MODIFICATIONS FOR MSS 29.2

- 2008 Add ^[...^;...^] features to formline\_(loa\_). These controls implement if/then/else and case selection facilities.
- 2027 Initialize tc\_data.ncpu to 0, rather than 1, so that system\_performance\_graph command reports proper number of CPUs configured.
- 1119 Add error\_table\_ codes to support new convert\_date\_to\_binary\_ subroutines, decode\_clock\_value\_ and encode\_clock\_value\_.
- 1291 Fix bug which prevented pc\_config from evicting out-of-service and RSW pages from core frames required for abs-wiring of contiguous pages.
- 1645 Add per-process segment fault, vtoc read, and vtoc write counts to values returned by hcs\_\$get\_process\_usage. New include segment process\_usage.incl.pl1 defines revised structure accepted by hcs\_\$get\_process\_usage.
- 1787 Modify ring brackets on >system\_library\_1>definitions\_ to be 0 5 5.
- 2071 Change set\_lock\_ to create an event channel only when such a channel is required (when the lock is locked).
- 2103 Meter directory activations on a system-wide basis.

## I. SYSTEM CHANGE LOG (continued)

## MCR MODIFICATIONS FOR 29.2a

NONE Remove support for quick PL/I put edit statements. This code needs further debugging.

## II. SEGMENT CHANGE LOG

## SEGMENT NAME

activate.pl1  
boundfault.pl1  
error\_table\_.et  
formline\_.alm  
get\_process\_usage.pl1  
hardcore.header  
pc\_config.pl1  
ods.cds  
pl1\_operators\_.mexp  
put\_field\_.alm  
put\_format\_.alm  
seg\_fault.pl1  
set\_lock\_.pl1  
tc\_data.cds  
vtoc\_man.pl1

## BOUND SEGMENT

R bound\_system\_faults  
R bound\_system\_faults  
R -  
R bound\_sss\_wired\_  
R bound\_process\_creation  
R -  
R bound\_page\_control  
R -  
R bound\_sss\_wired\_  
R bound\_sss\_wired\_  
D bound\_sss\_wired\_  
R bound\_system\_faults  
R bound\_ipc\_  
R -  
R bound\_vtoc\_man

## BIND SEGMENTS

bound\_sss\_wired\_.bind

R

## INCLUDE SEGMENTS

process\_usage.incl.pl1  
sst.incl.pl1

A  
R