

**Multics Task Report**

**MTR - 037**

**FROM: R. B. Snyder**

**TO: Distribution**

**DATE: 03/29/74**

**SUBJECT: New TTYDIM/Phase 1 Status**

**I. SIGNIFICANT EVENTS**

This is the first Multics Task Report for this project and as such, it lists only the various tasks required to fulfill the work specified in Phase 1 of the new TTYDIM (see MTB-049). No actual work has begun on this project as yet.

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TASK DESCRIPTION	PERSONNEL	START	FINISH	M-W	CHANGES-STATUS
Gross design device control Interpreter and macros.	RBS MJG RSC	EH15	EH16	13	
Gross design DIA routine.	RBS RSC MJG	EH16	EH17	13	
Gross design LSLA routine.	RBS RSC MJG	EH17	EH18	13	
Gross design HSLA software.	RBS MJG RSC	EH18	EH19	13	
Detail design device control Interpreter.	RSC	EH19	EH22	13	
Write device control statements.	MJG	EH19	EH23	14	
Document 355 simulator and two-process simulator.	RBS	EH19	EH20	1	
Alter BLAST to work with new 355/6180 interface.	RBS	EH20	EH21	1	
Detail design LSLA software.	RSC	EH22	EH25	14	
Detail design HSLA software.	MJG	EH23	EH29	16	
Code device control Interpreter.	RSC	EH26	EH28	12	
Code LSLA software.	RSC	EH28	EH32	14	

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PROJECT Hardcore Development Project AREA New TTYDIM/Phase 1

TASK DESCRIPTION	PERSONNEL	SIARI	FINISH	M-W	CHANGES-SIARIUS
Code HSLA software.	MJG	FH29	FH35	16	
Code DIA software.	RSC	FH32	FH35	3	
Vacation	MJG RSC	FH35	FH37	4	This is here only as a place holder as real vacation dates are not known yet.
Debug on 355 simulator.	MJG RSC	FH37	FH45	8	
Modify DN355 to use new interface and copy input to tty_buf.	RSC	F543	FH48	3	
Modify tty_read/tty_write.	MJG	FH45	FH47	2	
Change Answering Service to read answerbacks.	MJG	FH47	FH48	1	
Debug on development machine (assumes 4 hours/day on CISL development machine)	MJG RSC	FH48	FH4	16	

