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TO: Distribution

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SUBJECT: New TTYDIM/Phase 1 Status

I. SIGNIFICANT EVENTS

Detailed design and coding has been completed on the control tables, the control table interpreter, and the Isla software. In addition, the control table interpreter has been debugged in a stand-alone environment using the 355 simulator.

TASK DESCRIPTION	PERSONNEL	START	FINISH	M-W	CHANGES-SIAIUS
Gross design device control Interpreter and macros.	RBS MJG RSC	EW15 FW15	EW16 FW16	3 3	Done
Gross design DIA routine.	RBS RSC MJG	EW16 FW16	EW17 FW16	3 2	Done
Gross design LSLA routine.	RBS RSC MJG	EW17 FW16	EW18 FW17	3 2	Done
Gross design HSLA software.	RBS MJG RSC	EW18 FW17	EW19 FW17	3 2	Done. Did DN355 gross design and specified 6180 ttydim changes in remaining time
Detail design device control Interpreter.	RSC	EW19 FW19	EW21 FW20.4	2 1.6	Done
Write device control statements.	MJG	EW19 FW19	EW23 FW23	4 3	Done
Document 355 simulator and two-process simulator.	RBS	EW19 FW19	EW20 FW21	1 2	Done
After BLAST to work with new 355/6180 interface.	RBS	EW20	EW21	1	
Detail design DIA routine.	RSC	EW21 FW20.4	EW23 FW22.2	2 1.6	Done
Detail design LSLA software.	RSC	EW23 FW22.2	EW26 FW24	3 1.8	Done
Detail design HSLA software.	MJG	EW23 FW27	EW29	6	Half finished. Work delayed by MSD2400 commitments.

PROJECT _____ Hardcore Development Project _____ AREA _____ New TTYIM/Phase 1 _____

TASK DESCRIPTION	PERSONNEL	START	FINISH	M-W	CHANGES-STATUS
Code device control interpreter.	RSC	EM26 EM24	EM28 EM26	12 12	Done (including partial checkout using 355 simulator).
Code LSLA software.	RSC	EM28 EM28	EM32	14	Coding done. About to start partial checkout using 355 simulator.
Code HSLA software.	MJG	EM29	EM35	16	
Code DIA software.	RSC	EM32	EM35	13	
Vacation	MJG RSC	EM35	EM37	14	This is here only as a place holder as real vacation dates are not known yet.
Debug on 355 simulator.	MJG RSC	EM37	EM45	18	
Modify DN355 to use new Interface and copy input to tty_buf.	RSC	EM45	EM48	13	
Modify tty_read/tty_write.	MJG	EM45	EM47	12	
Change Answering Service to read answerbacks.	MJG	EM47	EM48	11	
Debug on development machine (assumes 4 hours/day on CISL development machine)	MJG RSC	EM48	EM04	16	

