

FROM: M. J. Grady
TO: Distribution
DATE: 11/11/74
SUBJECT: New TTYDIM/Phase 1 Status

1. SIGNIFICANT EVENTS

The project is progressing well and appears to be nearly on schedule. This was made possible with help from MIT with the loan of Frank Canali to the project. Frank has been working on the coding and testing of the new 6180 interface to the 355. We also hope to have him spend some time optimizing the conversion code in `tty_read` and `tty_write` to make better use of the EIS PL/I compiler.

We also appear to have solved most of the problems associated with using NPS on Multics. We have gone the route of mutual coexistence, allowing multiple 355's, each of which may run different software. Work will have to be done to develop a common `dn355` interface which would consist of different Multics 355 interface routines, called by the `ttydim` through a transfer vector. This will require some changes to Canali's work, but it does not appear difficult.

Design and coding of the HSLA manager is complete, including partial checkout on the 355 simulator. Coding of the LSLA and DIA managers is complete, including their partial checkout. This completes the major coding effort, and work is progressing on getting the other 355 modules (initialization, utilities, scheduler, trace, etc.) into shape for use with the new code. When this is completed the 355 core image will be built and tested on the 355 simulator, in preparation for two process simulation with the 6180 software.

TASK DESCRIPTION	PERSONNEL	START	FINISH	M-W	CHANGES-STATUS
Gross design device control interpreter and macros.	RBS MJG RSC	FW15 FW15	FW16 FW16	3 3	Done
Gross design DIA routine.	RBS RSC MJG	FW16 FW16	FW17 FW16	3 2	Done
Gross design LsLA routine.	RBS RSC MJG	FW17 FW16	FW18 FW17	3 2	Done
Gross design HsLA software.	RBS MJG RSC	FW18 FW17	FW19 FW17	3 2	Done
Gross design DN355 and specify 0180 ttydim changes.	RBS MJG RSC	FW17 FW17	FW18 FW18	2 2	Done
Detail design device control interpreter.	RSC	FW19 FW19	FW21 FW20.4	2 1.6	Done
Write device control statements.	MJG	FW19 FW19	FW23 FW23	4 3	Done
Document 355 simulator and two-process simulator.	RBS	FW19 FW19	FW20 FW21	1 2	Done
Alter BLAST to work with new 555/0180 interface.	RBS MJG	FW20	FW21	1	Will be done when time permits.
Detail design DIA routine.	RSC	FW21 FW20.4	FW23 FW22.2	2 1.6	Done
Detail design LsLA software.	RSC	FW23 FW22.2	FW26 FW24	3 1.8	Done
Detail design HsLA software.	MJG	FW23 FW27	FW29 FW34	6 5	Done. Work delayed by MDS2400 commitments.

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TASK DESCRIPTION	PERSONNEL	START	FINISH	M-W	CHANGES-STATUS
Code device control interpreter.	RSC	EW26 FW24	EW28 FW26	2 2	Done (including partial checkout using 355 simulator).
Code LSLA software.	RSC	EW28 FW28	EW32 FW33.1	4 5	Done (including partial checkout using 355 simulator).
Code iLSLA software.	MJG	EW29 FW34	EW35 FW43	6 6	Done (including partial checkout using 355 simulator).
Code DIA software.	RSC	EW32 FW33.2	EW35 FW39.2	3 4	Done (including partial checkout using 355 simulator).
Vacation	MJG RSC	EW35 FW35	EW37 FW37	4 4	Coren done. Grady 3 days left.
Modify 355 initialization and utilities.	RSC	EW38 FW39.3	EW42 FW42	4 3	Done.
Modify 355 scheduler, console manager and trace.	MJG	EW43 FW43	EW47	4	Started on scheduler.
Debug on 355 simulator, 355 code only.	MJG RSC	EW47	EW50	6	
Modify DN355 to use new interface and copy input to tty_buf.	FAC	EW45 FW35	EW48 FW39	3 4	Done.
Modify tty_read/tty_write.	FAC	EW45 FW39	EW47 FW44	2 5	Done.
Change Answering Service to read answerbacks.	FAC	EW45	EW47	2	

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TASK DESCRIPTION	PERSONNEL	START	FINISH	M-W	CHANGES-STATUS
Debug v180 code.	FAC	FW47	FW50	3	Some special purpose tools required.
Two process debugging, using simulator.	MJG RSC FAC	FW50	FW01	12	
Debug on development machine, 4 hours/day.	MJG RSC FAC	FW01	FW06	15	