

J. Saltzer
For your info

Honeywell

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NOV 23 1973
J. H. SALTZER

FYI
Shaw
Conrad

To: Attendees

cc: CT Clingen
JW Gintell
RE Hoffman

Subject: FORUM Minutes for 6 November 1973

Attendees: WJ Burner RM Gross RA Roach
 RJ Chevalier DA Kayden LJ Ryan
 RC Daley ED Kleinow JR Steinberg

Hardware. ED Kleinow

--Status of priority 1 problems

- CSL instruction gets op-not-completes - FED has fix in-house. Wire change planned for Wednesday, 7 November 1973
- CSL instruction sometimes ignores PR modifications for operand 2. (CPU-B) - appears to be a component failure. FED will track this one.
- MLR instruction sometimes fails at page boundaries - fix in-house; will install on 7 November 1973.
- CMPN instruction sometimes fails with large exponents - fix in-house; will install on 7 November 1973.

--CSM5 - failed on development side 6 November 1973. Also T/D's failed. FED will follow up.

--190's - Area 11 down - part at airport - FED will fix.

--LSLA - checked out on Sunday, 4 November 1973. FED located and replaced several bad chips.

--MPC181 - DR board replaced - caused development to be down on two sessions.

--PRT301 - Printer hood problem corrected. RA Roach noted that "paper low" signals from the 301 are sometimes false. FED will investigate.

FCO. RM Gross

FCO's noted in earlier meetins as requiring large blocks of time to install are now being scheduled as board swaps. These will allow a reduction of 20 hours in FCO's outstanding.

Presently, outstanding FCO's are: 30 hours on 355's; 65 hours on 190's, MTH500's, SCU; 9 hours on hold. Most of these FCO's will require system time.

SOFTWARE. RA Roach

--Two crashes during week.

- Initializer waiting on process destruct. T. VanVleck will submit fix within a week for installation within two weeks.
- Mailbox time out - still investigating. Snider is making patches on code to resolve these problems as they occur.

--There are four priority 1 bugs. Fixes for three of these (201, 195, 163) are to be installed in release 21.3 (#195) and 21.4 (#201, 163). The remaining priority 1 bug is assigned to and be tracked by DM Wells. ←

--RC Daley requests HISI posture relative to ARDS support at 1200 baud.

Equipment to be installed

- MTH500's - Dual channel will arrive in 3 weeks. Two drives will be installed as soon as the necessary dual channel arrives.
- 190 Dual Channel - necessary DLT cables will ship within 3 weeks also. Installation to follow.

Other

In response to earlier request for information, HISI quotes delivery time of LSLA and cabinet at 10 weeks ARO.

LJ Ryan asked for physical specifications on this drive.

RJC/kmp