

# Honeywell

*J. H. Saltzer*  
*For your info*  
F71: *clerk*  
*Schuyler*

RECEIVED  
FEB 28 1974  
J. H. SALTZER

To: List of Attendees  
Subject: FORUM Minutes for 19 February 1974

cc: CT Clingen  
JW Gintell  
RE Hoffman

Attendees: WJ Burner                      DM Jordan                      RA Roach  
              RJ Chevalier                DA Kayden                    LJ Ryan  
              RM Gross                      ED Kleinow                  JR Steinberg

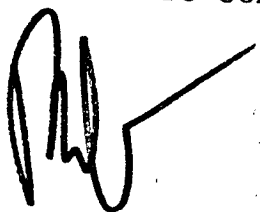
HARDWARE. ED Kleinow

- Processor A - CA board failed. Board replaced and ran okay.
- DSU191 - Problems appear to be caused by bad track on disk. FED will investigate.
- MTH500 - Large number of recoverable write errors is of extreme concern to MIT. FED indicates that the recoverable error rate is quite satisfactory per engineering standards.
- Processors - CMPC change is in and checked out. This problem was listed as Bug #63 on the hardware bug list.
- Mem-C - Store B - Parity errors were caused by component mis-adjustment. FED adjusted and it ran okay.
- Bulk Store - Replacement core (EMI) expected this week. Tentative installation plan is to install this weekend, 2/23/74.

SOFTWARE. DM Jordan

--H6180 Software Bug List

- #243 - Ring 0 NCP is Version 1 pl-1 - Doug Wells is assigned to this problem and is presumed to be working on it. *← Bug \$ been!*
- #224 - ioat overflow - fix installed today - not checked out.
- #198 - possibly lost notify - S. Webber assigned.
- #157 - ARDS problem - this one seems to have been taken care of. ARDS is being used productively.
- #257, 230, 218, 253, 256 and 194 - fixes for all these bugs are complete. Should be installed this morning.



RHC/kmp