

F O R U M L I S T

RECEIVED

OCT 8 1975

J. H. SALTZER

G. Ackerman-Lewis

D. Clark

W. Burner

C. Clingen

R. Daley

J. Gintell

G. Lundberg

J. Regester

L. Ryan

Prof. J. Saltzer

R. Shoemaker

J. R. Steinberg

Honeywell

TO: List of Attendees cc: List

SUBJECT: Forum Minutes for September 30, 1975

ATTENDEES: R. J. Chevalier W. McFall
D. M. Jordan R. Moore
P. B. Kelley R. A. Roach
E. D. Kleinow

Hardware - E. D. Kleinow

- * Processors
Startup faults have not reappeared since FED placed a jumper wire on initialize signal on processor B fault board.

Jordan says one (startup fault) occurred on 18 Sept. but it may not have been same problem.
- * DSU 190A
Intermittent problems traced back to failing memory chip on SB board in MPC.

FED replaced board, and it now seems okay.
- * MTH500
New head installed on unit 1. Appears to be running fine on the basis of a successful save while using this unit. A new head will be installed in unit 3 soon. Present head performance is marginal.
- * Punch
FED replaced broken emitter lamp.
- * PRT3Q1
Hood shear pin broke again.

FED replaced it and made adjustments to the hood raising mechanism.

Software - D. M. Jordan

- * MSS 26.5 and 27.0 installed
- * TOLTS installed
- * ONE Software crash. ITT overflow problem.

Billerica

* DSU190-B

Head crash on AREA Ø. Unit still giving some errors. FED will replace head. Disk pack condition is questionable.

Users are on system with Priority Scheduler turned on. 20% MAX currently allocated to Billerica.

BA