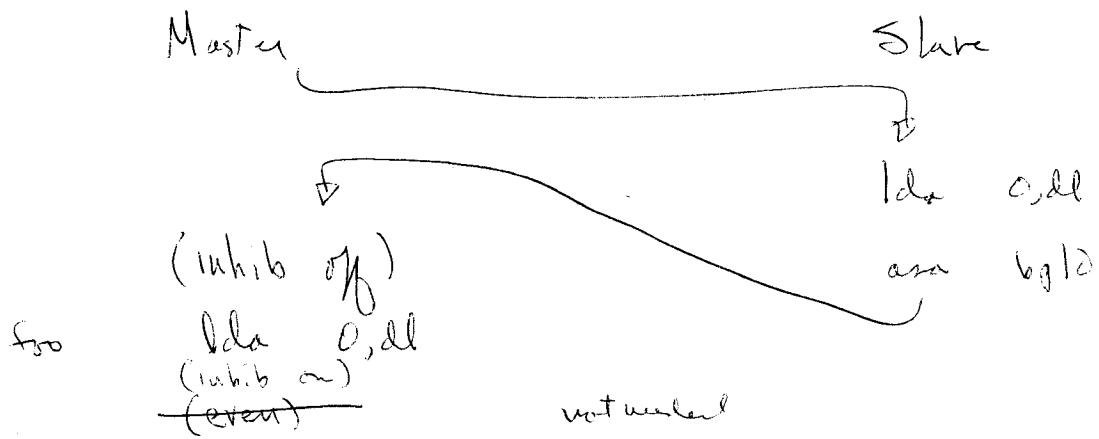


# STAL simulator



asc	bpl0
cos	syslock
tuZ	foo
ldc	bpl0
tuZ	*+2
stg	bpl0
ldc	=-1
sta	syslock
inhib	N

upset ~ some thing